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..TITLE HA1NDEC*11-DCKBR*4
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..PROGRAM BY BRUCE BURGESS

OPERATIONAL SWITCH SETTINGS

```

I
I SWITCH USE
I *****
I 15 HALT ON ERROR
I 14 LOOP ON TEST
I 13 INHIBIT ERROR TYPEOUTS
I 11 INHIBIT ITERATIONS
I 10 0 = BELL ON PASS COMPLETE
I 9 1 = BELL ON ERROR
I 8 LOOP ON ERROR
I 0 LOOP ON TEST IN SW*116>
I
I SPECIAL USER TYPE SWITCH SW*12>
I
I IF SET INDICATES USER INPUT
I IF CLEAR INDICATES PROGRAM FIND
I
I SPECIAL RT11 DISABLE SWITCH SW*00>
I
I IF SET INDICATES DON'T USE IF PRESENT
I IF CLEAR INDICATES ALLOW USE IF PRESENT

```

BASIC DEFINITIONS

```

I*****
I INITIAL ADDRESS OF THE STACK POINTER
I STACK= 1100
I*****
I EQUIV EMT,HLT BASIC DEFINITION OF ERROR CALL
I EQUIV IOY,SCOPE BASIC DEFINITION OF SCOPE CALL
I PS= 177776 I PROCESSOR STATUS WORD
I EQUIV PS,PSM
I SWR= 177576 I SWITCH REGISTER
I DISPLAY=SWR

```

REGISTER DEFINITION

```

I R0= 20 I GENERAL REGISTER
I R1= 21 I GENERAL REGISTER
I R2= 22 I GENERAL REGISTER
I R3= 23 I GENERAL REGISTER
I R4= 24 I GENERAL REGISTER
I R5= 25 I GENERAL REGISTER
I R6= 26 I GENERAL REGISTER
I R7= 27 I GENERAL REGISTER
I EQUIV R6,SP I STACK POINTER
I EQUIV R7,PC I PROGRAM COUNTER

```

SWITCH DEFINITION

SW15= 100000

55
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108

```

SW14= 40000
SW13= 20000
SW12= 10000
SW11= 4000
SW10= 2000
SW09= 1000
SW08= 400
SW07= 200
SW06= 100
SW05= 40
SW04= 20
SW03= 10
SW02= 4
SW01= 2
SW00= 1
I EQUIV SW09,SW9
I EQUIV SW08,SW8
I EQUIV SW07,SW7
I EQUIV SW06,SW6
I EQUIV SW05,SW5
I EQUIV SW04,SW4
I EQUIV SW03,SW3
I EQUIV SW02,SW2
I EQUIV SW01,SW1
I EQUIV SW00,SW0

```

MISCELLANEOUS BIT ASSIGNMENT

```

BIT15= 100000
BIT14= 40000
BIT13= 20000
BIT12= 10000
BIT11= 4000
BIT10= 2000
BIT09= 1000
BIT08= 400
BIT07= 200
BIT06= 100
BIT05= 40
BIT04= 20
BIT03= 10
BIT02= 4
BIT01= 2
BIT00= 1
I EQUIV BIT09,BIT9
I EQUIV BIT08,BIT8
I EQUIV BIT07,BIT7
I EQUIV BIT06,BIT6
I EQUIV BIT05,BIT5
I EQUIV BIT04,BIT4
I EQUIV BIT03,BIT3
I EQUIV BIT02,BIT2
I EQUIV BIT01,BIT1
I EQUIV BIT00,BIT0

```

```

109          VECTOR ADDRESSES
110          EHRVEC= 4
111          RESVEC= 10
112          TRIVEC=14
113          TRIVEC= 14
114          BPTVEC= 14
115          IDTVEC= 20
116          PRRVEC= 24
117          ENTVEC= 30
118          THAPVEC=34
119          N=1
120
121
122
123
124          .#6
125          ITRAP CATCHER IN UNUSED LOCATIONS FROM 0 - 776
126          ILOCATION 0 WILL CATCH IMPROPERLY LOADED VECTORS
127
128          .#200
129
130          JMP      000137 001700      IJUMP TO STARTING ADDRESS OF PROGRAM
131
132          .#46
133          SENDAD
134          .#52
135          01114
136
137          .#204
138
139          IRT11-0 STATUS REGISTER ADDRESSES
140
141          SM01 177572
142          SM21 177576
143
144          IINTERNAL PAGE DESCRIPTOR REGISTERS
145
146          KPOR01 172300
147          KPOR11 172302
148          KPOR21 172304
149          KPOR31 172306
150          KPOR41 172310
151          KPOR51 172312
152          KPOR61 172314
153          KPDY1 172316
154
155          IINTERNAL PAGE ADDRESS REGISTERS
156
157          KPAR01 172340
158          KPAR11 172342
159          KPAR21 172344
160          KPAR31 172346
161          KPAR41 172350
162          KPAR51 172352
  
```

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163          KPAR61 172354
164          KPART1 172356
165
166          IRT11 VECTOR ADDRESS
167
168          SEGVEC1 250,252
  
```

```

169
170
171          001100          I*****
172          ,P1100
173
174          ROUTINE TO TYPE ASCII MESSAGE, MESSAGE MUST TERMINATE WITH A 0 BYTE.
175          THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
176          PROTE1: SHULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
177          PROTE2: SFILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
178
179          FCALLS
180          I) USING A TRAP INSTRUCTION
181          I          TYPE          ,MESADR          I/MESADR IS FIRST ADDRESS OF AN ASCII STRING
182          I          TYPE          ,MESADR
183          I          TYPE          ,MESADR
184
185          J) USING A JSR INSTRUCTION
186          I          MOV          PS,*(SP)          I/PUSH PROCESSOR STATUS WORD ON THE STACK
187          I          JSR          PC,SFYPE          I/CALL TYPE ROUTINE
188          I          MESADDR          I/FIRST ADDRESS OF MESSAGE
189
190          01PS: 177564          I/TTY PRINTER STATUS REG. ADDRESS
191          01PE: 177566          I/TTY PRINTER BUFFER REG. ADDRESS
192          01SHULL: ,BYTE 0          I/CONTAINS NULL CHARACTER FOR FILLS
193          01SFILLS: ,BYTE 2          I/CONTAINS # OF FILLER CHARACTERS REQUIRED
194          01SFPLG: ,BYTE 0          I/TERMINAL AVAILABLE FLAG (0=YES)
195          01SRESV: ,BYTE 0          I/RESERVED
196
197          01S1: 001110 105767 177772 01S1: TSYB STPFLG          I/IS THERE A TERMINAL?
198          001114 001402          I/IF YES
199          001116 000000          I/HALT HERE IF NO TERMINAL
200          001120 000007          I/LEAVE
201          001122 010006          I/SAVE R0
202          001124 017600 000000          I/GET ADDRESS OF ASCII STRING
203          001130 112006          I/PUSH CHARACTER TO BE TYPED ONTO STACK
204          001132 001005          I/IF IT ISN'T THE TERMINATOR
205          001134 005726          I/IF TERMINATOR POP IT OFF THE STACK
206          001136 012600          I/RESTORE R0
207          001140 002716 000002          I/ADJUST RETURN PC
208          001144 000002          I/RETURN
209          001146 004767 000006          I/GO TYPE THIS CHARACTER
210          001152 122726 000012          I/CHECK IF THE CHAR. TYPED WAS A LINE FEED
211          001156 001364          I/GO GET NEXT CHAR. IF NOT LINE FEED
212          001160 016746 177726          I/GET # OF FILLER CHARS. NEEDED
213          I/AND THE NULL CHAR.
214          001164 105366 000001          I/DOES A NULL NEED TO BE TYPED?
215          001170 002770          I/IF NO--GO POP THE NULL OFF OF STACK
216          001172 004767 000002          I/GO TYPE A NULL
217          001176 000772          I/LOOP
218          001200 105777 177674          I/WAIT UNTIL PRINTER IS READY
219          001204 102375          I/LOAD CHAN TO BE TYPED INTO DATA REC.
220          001206 116177 000002 177666          I/PRESERVE SOME MORE CORE FOR OVERLAY CAPABILITIES
221          001214 002007          I/
222          001216 000002          I/

```

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223          I*****
224          I/COMMON TAGS
225          ,=1300
226
227          001300          I/CONTAINS PASS COUNT
228
229          001300 000000          I/CONTAINS THE TEST NUMBER
230          001302 000000          I/CONTAINS SURTEST ITERATION COUNT
231          001304 000000          I/CONTAINS SCOPE LOOP ADDRESS
232          001306 000000          I/CONTAINS SCOPE RETURN FOR ERRORS
233          001310 000000          I/CONTAINS TOTAL ERRORS DETECTED
234          001312 000000          I/CONTAINS ERROR FLAG
235          001314 000          I/RESERVED--NOT TO BE USED
236          001316 000          I/RESERVED--NOT TO BE USED
237          001318 000000 000000          I/CONTAINS ITEM CONTROL BYTE
238          001322 000          I/RESERVED--NOT TO BE USED
239          001324 000          I/CONTAINS PC OF LAST HLT INSTRUCTION
240          001326 000000          I/CONTAINS ADDRESS OF 'GOOD' DATA
241          001328 000000          I/CONTAINS ADDRESS OF 'BAD' DATA
242          001330 000000          I/CONTAINS 'GOOD' DATA
243          001332 000000          I/CONTAINS 'BAD' DATA
244          001334 000000          I/CONTAINS THE ADDRESS FROM
245          001336 000000          I/WHICH (*REG0) WAS OBTAINED
246
247          001340 000000          I/CONTAINS ((SREGAD)+0)
248          001342 000000          I/CONTAINS ((SREGAD)+2)
249          001344 000000          I/CONTAINS ((SREGAD)+4)
250          001346 000000          I/CONTAINS ((SREGAD)+6)
251          001350 000000          I/CONTAINS ((SREGAD)+10)
252          001352 000000          I/CONTAINS ((SREGAD)+12)
253          001354 000000          I/CONTAINS ((SREGAD)+14)
254          001356 000000          I/CONTAINS ((SREGAD)+16)
255          001360 000000          I/CONTAINS ((SREGAD)+20)
256          001362 000000          I/CONTAINS ((SREGAD)+22)
257          001364 000000          I/CONTAINS ((SREGAD)+24)
258          001366 000000          I/USER DEFINED
259          001370 000000          I/USER DEFINED
260          001372 000000          I/USER DEFINED
261          001374 000000          I/USER DEFINED
262          001376 000000          I/USER DEFINED
263          001400 000000          I/USER DEFINED
264          001402 000000          I/USER DEFINED
265          001404 000000          I/USER DEFINED
266          001406 000000          I/USER DEFINED
267          001410 000000          I/USER DEFINED
268          001412 000000          I/USER DEFINED
269          I/FINE FOLLOWING TAG(S) ARE USER DEFINED
270          001414 000000          I/SIMPAD1 ,WORD 0
271          001416 000000          I/SSETA01 ,WORD 0
272          001420 000000          I/SSETB1 ,WORD 0
273          001422 000000          I/SSET11 ,WORD 0          I/THSE LOCATIONS CONTAIN THE
274          001424 000000          I/SSET21 ,WORD 0          I/APPROPRIATE OFFSET VALUES
275          001426 000000          I/SSET31 ,WORD 0          I/FOR THE PARRY CONTROL
276          001430 000000          I/SSET41 ,WORD 0          I/REGISTERS WHEN MEMORY

```

277 001432 000000
278 001434 000000
279 001436 000000
280 001440 000000
281 001442 000000
282 001444 000000
283 001446 000000
284 001450 000000
285 001452 000000
286 001454 000000
287 001456 000000
288 001460 000000
289 001462 000000
290 001464 000000
291 001466 000000
292 001470 000000
293 001472 000000
294 001474 000000
295 001476 000000
296
297

2NET51 .WORD 0
2NET61 .WORD 0
2NET71 .WORD 0
2NET81 .WORD 0
2NET111 .WORD 0
2NET121 .WORD 0
NICRAD1 .WORD 0
NIERS1 .WORD 0
NIER11 .WORD 0
NIER21 .WORD 0
NIER31 .WORD 0
NIER41 .WORD 0
NIER51 .WORD 0
NIER61 .WORD 0
NIENY1 .WORD 0
NIER101 .WORD 0
NIER111 .WORD 0
NIER121 .WORD 0
NENSTK1 .WORD 0
/END OF USER DEFINED TAG(S)

LOADING PROGRAM EXECUTION

THESE LOCATIONS CONTAIN THE
APPROPRIATE INTERLEAVE FACTORS
FOR THE PARITY CONTROL
REGISTERS (IF ANY)

298
299
300
301
302
303
304
305
306
307
308
309
310 001500
311
312
313
314 001500 013152
315 001502 015005
316
317
318 001504 015142
319 001506 000000
320
321 001510 013776
322 001512 014470
323
324 001514 015106
325 001516 000000
326
327 001520 014027
328 001522 014604
329
330
331
332
333
334 001524 015124
335 001526 000000
336
337 001530 014055
338 001532 014540
339 001534 015114
340 001536 000000
341
342 001540 014120
343 001542 014470
344
345 001544 015106
346 001546 000000
347
348 001550 014145
349 001552 014566
350 001554 015120
351 001556 000000

THE FOLLOWING TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
THIS INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
LOCATION ITEMS. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
NOTE1: IF ITEM# IS 0 THE ONLY PERTINENT DATA IS (SHLTAD).
NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

EM POINTS TO THE ERROR MESSAGE
DH POINTS TO THE DATA HEADEN
DT POINTS TO THE DATA
DF POINTS TO THE DATA FORMAT

ERRORS:
NOTE: ALL NUMBERS ARE TYPED AS 6-DIGIT OCTAL NUMBERS

ITEM 1
EM1 ITEM DIDN'T ABORT
DH5 PROGRAM PC
DT5 REGISTER UNDER TEST
DF EXPECTED ABORT PC
0 SHLTAD, PARITY, SGDDAY

ITEM 2
EM2 FATAL ERROR TO PROGRAM
DH1 PROGRAM PC
DT1 REGISTER UNDER TEST
DF SHLTAD, PARITY

ITEM 3
EM3 ABORTED INCORRECTLY
DH4 PROGRAM PC
DT1 REGISTER UNDER TEST
DF EXPECTED BITS 5 THRU 11
0 ACTUAL BITS 5 THRU 11
0 EXPECTED ABORT PC
0 ACTUAL ABORT PC
0 SHLTAD, PARITY, SGDAOR, SBDADR, SGDDAY, SDDAY

ITEM 4
EM4 NO PARITY MEMORY FOUND BELOW 28K
DH2 REGISTER UNDER TEST
DT2 PARITY
0

ITEM 5
EM5 RESET DOESN'T WORK
DH1 PROGRAM PC
DT1 REGISTER UNDER TEST
DF SHLTAD, PARITY

ITEM 6
EM6 USER SELECTED REGISTER NOT PRESENT
DH3 PROGRAM PC
DT3 SHLTAD
0

Line	Address	PC	Code	Description
352				
353	001560	010212	EMT	NO PARITY MEMORY FOUND AT ALL
354	001562	014540	DM2	REGISTER UNDER TEST
355	001564	015114	DT2	PARITY
356	001566	000000	0	
357				
358	001570	014252	EM10	DIDN'T ABORT OR RECOGNIZE
359				STACK VIOLATION
360	001572	015005	DM5	PROGRAM PC
361				REGISTER UNDER TEST
362				EXPECTED ABORT PC
363	001574	015142	DT5	SHLTD, PARITY, SCDDAT
364	001576	000000	0	
365				
366	001600	014326	EM11	ABORTED BUT STACK VIOLATION
367				NOT RECOGNIZED
368	001602	014470	DM1	PROGRAM PC
369				REGISTER UNDER TEST
370	001604	015106	DT1	SHLTD, PARITY
371	001606	000000	0	
372				
373	001610	014403	EM12	STACK VIOLATION PICKED UP BUT
374				ABORT NOT RECOGNIZED
375	001612	014470	DM1	PROGRAM PC
376				REGISTER UNDER TEST
377	001614	015106	DT1	SHLTD, PARITY
378	001616	000000	0	

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430

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```

/*****
/
/SYSTEM PARITY REGISTER NOTES FOR M11 AND M811
/
/*****
/
/BIT ASSIGNMENTS FOR THE M11 PARITY REGISTER IS AS FOLLOWS:
/
/BI15          PARITY ERROR
/BI15 11-5    ERROR ADDRESS          HIGH ORDER ADDRESS BITS
/                                     (OF ADDRESS OF MOST RECENT ERROR
/                                     (BITS 17 THRU 11)
/INORMAL PARITY (ODD) WHEN CLEAR
/OTHER PARITY (EVEN) WHEN SET
/NO ACTION WHEN CLEAR
/TRAP TO VECTOR 114 WHEN SET
/
/NOTE: THE ABOVE BITS ARE READ/WRITE AND CAN BE CLEARED BY 'INIT' (EXCEPT BITS 11-5)
/
/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/\/
/
/BIT ASSIGNMENTS FOR THE M811 PARITY REGISTER IS AS FOLLOWS:
/
/BI15          PARITY ERROR
/BIT02         WRITE                  INORMAL PARITY (EVEN) WHEN CLEAR
/                                     /OTHER PARITY (ODD) WHEN SET
/BIT00         ERROR ACTION ENABLE    /NO ACTION WHEN CLEAR
/                                     /TRAP TO VECTOR 114 WHEN SET
/
/NOTE: THERE ARE NO ERROR ADDRESS BITS IN THE CURENRY M811 PARITY REGISTER
/ HOWEVER, THERE WILL BE IN A LATER VERSION WHICH WILL BE
/ HANDLED PROPERLY BY THIS PROGRAM
/
/*****
/
/SPECIAL NOTE---THERE ARE 2 GENERAL PURPOSE REGISTERS USED IN THE
PROGRAM FOR SPECIFIC CIRCUMSTANCES, THEY ARE:
/
/ R1 - WILL ALWAYS CONTAIN THE 1ST ADDRESS OF THE 2
/ LOCATION MAP USED FOR TESTING,
/ THE CONTENTS OF R1 IS DETERMINED BY THE 'COMPUT'
/ ROUTINE SHOWN FURTHER DOWN,
/ EXAMINATION OF R1 WILL TELL YOU WHERE IN PARITY
/ MEMORY TESTING IS BEING CONDUCTED,
/
/ R5 - WILL ALWAYS CONTAIN THE ADDRESS OF THE ROUTINE
/ FOR SETTING UP THE PARITY VECTOR SERVICE ADDRESS,
/
/*****
/*****

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431
432
433
434
435
436
437
438
439 001620 000114
440 001622 000000
441 001624 000000
442
443
444
445 001626 000000
446
447 001630 000000
448
449
450
451 001632 000000
452
453
454
455
456 001634 000000
457
458
459
460
461
462
463
464 001636 000000
465
466
467 001640 000000
468
469 001642 000000
470
471
472
473
474
475 001644
476 001644 000003 000001
477 001650 000004 000002
478 001654 000011 000003
479 001660 000014 000004
480 001664 000017 000005
481 001670 000022 000006
482 001674 000025 000007
483 001700 000030 000010
484 001704 000000
    
```

 MISCELLANEOUS COMMON PARITY VARIABLES AND FLAGS

ITRVCT 114
 PARITY 0
 PFCORZONES 0
 HRESFLAG 0
 USERTYPE 0
 BLKCNT 0
 RESTOREBASE 0
 LEAFCNT 0
 MEMADR 0
 CPUADR 0

 PARITY INTERRUPT VECTOR ADDRESSES
 (CONTAINS PARITY REGISTER IN USE
 FLAG TO INDICATE TO 'CHECKCLOC'
 ROUTINE THAT A PS OR PC PEICH
 FOR A ZONE ABORT WAS DONE
 I0 = NO; 1 = YES
 INDICATES PARITY TYPE
 I0 = CORE; 1 = NOS
 INDICATES USER SELECTION OF
 PARITY REGISTER
 I0 = PROGRAM FIND
 I1 = USER SELECTION
 (CONTAINS THE NUMBER OF CONSEC-
 UTIVE LOCATIONS TO BE TESTED
 DURING PROGRAM TABULATION TO
 COVER CASES OF MEMORY INTER-
 LEAVING
 I0 = PAGE 1 ADDRESS
 FOR CURRENT MEMORY ADDRESS FOR
 RESTORATION DURING
 RUNNING OF PROGRAM. IT IS USED IF
 WE HAVE CHECKED CONSECUTIVE
 LOCATIONS WITHOUT AN ABORT BEFORE
 GOING TO NEXT OFFSET WHICH WILL
 PUT US IN ANOTHER BANK
 (CONTAINS THE NO. OF ABORTS
 ENCOUNTERED IN DETERMINATION OF
 AN INTERLEAVE FACTOR
 (CONTAINS A BASE ADDRESS OR A
 CURRENT MEMORY ADDRESS USED IN
 FLAG TO INDICATE PROCESSOR
 I0 = 11/40; 1 = 11/40
 PARITY TABLE CREATION

 THE FOLLOWING TABLE IS USED TO DETERMINE THE
 INTERLEAVE FACTOR FOR THE CONTROL REGISTERS
 INTERTABLE:

3,1	15 ABORTS ON 3 CONSECUTIVE LOCS. = 1 WAY LEAVE
6,2	15 ABORTS ON 6 CONSECUTIVE LOCS. = 2 WAY LEAVE
9,3	15 ABORTS ON 9 CONSECUTIVE LOCS. = 3 WAY LEAVE
12,4	15 ABORTS ON 12 CONSECUTIVE LOCS. = 4 WAY LEAVE
15,5	15 ABORTS ON 15 CONSECUTIVE LOCS. = 5 WAY LEAVE
18,6	15 ABORTS ON 18 CONSECUTIVE LOCS. = 6 WAY LEAVE
21,7	15 ABORTS ON 21 CONSECUTIVE LOCS. = 7 WAY LEAVE
24,8	15 ABORTS ON 24 CONSECUTIVE LOCS. = 8 WAY LEAVE
0	END OF TABLE TERMINATOR

```

485
486 001706
487 001706 012706 001100
488 001712 012737 012307 000020
489 001720 012737 000300 000022
490 001726 005067 177353
491 001732 012737 012634 000030
492 001740 012737 000300 000032
493 001746 012737 013556 000034
494 001754 012737 000300 000036
495 001762 012737 013606 000024
496 001770 012737 000300 000026
497 001776 005067 177276
498 002002 005067 177276
499 002006 005067 012776
500 002012 105067 177276
501 002016 005067 177270
502 002022 005067 010706
503
504 002026 005037 001630
505
506
507 002032 005037 002304
508 002036 005037 001420
509 002042 005037 001422
510 002046 005037 001424
511 002052 005037 001426
512 002056 005037 001428
513 002062 005037 001422
514 002066 005037 001424
515 002072 005037 001436
516 002076 005037 001440
517 002102 005037 001442
518 002106 005037 001444
519 002112 005037 001450
520 002116 005037 001452
521 002122 005037 001454
522 002126 005037 001456
523 002132 005037 001460
524 002136 005037 001462
525 002142 005037 001464
526 002146 005037 001466
527 002152 005037 001470
528 002156 005037 001472
529 002162 005037 001474
530 002166 005037 001636
531
532 002172 005037 001642
533 002176 013706 000000
534 002202 013706 000010
535 002206 012737 002226 000010
536 002210 012737 000300 000012
537 002222 005037
538 002226 005037
    
```

BEGIN:

MOV	#STACK,SP	SETUP THE STACK POINTER
MOV	#SCOPE,#IOTVEC	LOAD VECTOR FOR SCOPE ROUTINE
MOV	#30,#IOIVEC+2	LEVEL 7
CLR	SYSTEM	INITIALIZE THE TEST NUMBER
MOV	#HLT,#ENTVEC	LOAD VECTOR FOR HLT(ERROR) ROUTINE
MOV	#30,#ENTVEC+2	LEVEL 7
MOV	#TRAP,#TRAPVEC	LOAD VECTOR FOR TRAP CALLS
MOV	#30,#TRAPVEC+2	LEVEL 7
MOV	#PWRDN,#PWRVEC	POWER FAILURE VECTOR
MOV	#30,#PWRVEC+2	LEVEL 7
CLR	SPASS	CLEAR THE PASS COUNT
CLR	SICNT	INITIALIZE THE ITERATION COUNTER
CLR	STIMES	INITIALIZE NUMBER OF ITERATIONS
CLR	#ERFLG	CLEAR THE ERROR FLAG
CLR	#ERTTL	CLEAR THE ERROR COUNT
CLR	#ESCAPE	CLEAR THE ESCAPE ON ERROR ADDRESS
CLR	#USERTYPE	SET USER SELECTION INDICATOR TO ZERO INDICATING PROGRAM TABULATION
CLR	#SKY11	CLEAR K11 PRESENCE FLAG
CLR	#SSET0	CLEAR THE OFFSET
CLR	#SSET1	TABLE LOCATIONS FOR
CLR	#SSET2	THE K11 OPTION
CLR	#SSET3	
CLR	#SSET4	
CLR	#SSET5	
CLR	#SSET6	
CLR	#SSET7	
CLR	#SSET10	
CLR	#SSET11	
CLR	#SSET12	
CLR	#ENTER0	CLEAR THE INTERLEAVE TABLE ENTRY LOCATIONS
CLR	#ENTER1	
CLR	#ENTER2	
CLR	#ENTER3	
CLR	#ENTER4	
CLR	#ENTER5	
CLR	#ENTER6	
CLR	#ENTER7	
CLR	#ENTER10	
CLR	#ENTER11	
CLR	#ENTER12	
CLR	#LEAFCNT	CLEAR NO. OF ABORTS PER NO. OF CONSECUTIVE LOCS. TESTED FLAG
MOV	#CPUADR	CLEAR PROCESSOR INDICATOR FLAG
MOV	#4,-(SF)	SAVE CONTENTS OF LOC. 4
MOV	#10,-(SF)	SAVE CONTENTS OF LOC. 10
MOV	#18,#RESVEC	SET UP FOR 'SPL' TRAP ADDRESS
MOV	#30,#RESVEC+2	SET UP FOR 'SPL' TRAP PS
SPL	7	ATTEMPT TO SET A PRIORITY LEVEL
BR	25	BRANCH INDICATING WE ARE ON AN

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539
540 002206 002206 13: CMP (SP)*,(SP)+ 751745 PROCESSOR
541 002208 005247 001840 INC @0PDR0 IRESET THE STACK FROM TRAP
542 ISET FLAG INDICATING WE ARE ON
543 IAN 11745 PROCESSOR
544 002208 012637 000010 20: MOV (SP)*,@#10 IRESTORE CONTENTS OF LOC. 10
545 002208 005247 001840 CLR @#12 IRESTORE TRAPCATCHER LOC. 12
546 002208 012737 002324 000004 MOV @KTTIMEOUT,@#ERRVEC ISET UP KI TIMEOUT ADDRESS
547 002208 005777 175720 000006 MOV @#00,@#ERRVEC+2 ISET UP KI TIMEOUT RS
548 002204 005077 175714 TST @#R0 IKT11 ARE YOU THERE?
549 002204 005077 175714 CLR @#R0 IYES - INITIALIZE IT IN CASE
550 IUSER DOESNT WANT IT
551 002270 013700 177570 MOV @#SHR,R0 IGET SHR CONTENTS
552 002276 006300 ASL R0 IMOVE BIT#6 TO BIT#7 POSITION
553 002276 105700 TST R0 IKT11 PRESENT (OBVIOUSLY) IF
554 IWE REACH THIS INSTRUCTION
555 002300 100412 BMI GO IDOES USER WANT IT?
556 002302 005327 (PC)+ IBRANCH IF NO
557 002304 000000 SKY11: 0 IYES - SET KY11 FLAG
558 ICONTAINS A *1 IF KY11 OPTION
559 IIS PRESENT
560 002310 004737 013734 JSR PC,@#SIZE ISEC HOW MUCH MEMORY IS AVAILABLE
561 002312 005077 175712 CLR @#PAR0 ICLEAR PAGE 0 OFFSET REGISTER
562 002316 005277 175060 INC @#R0 ITURN ON MEMORY MANAGEMENT
563 002322 000401 BR @#R0 ISKIP NEXT INSTRUCTION
564 002324 002206 K11TIMEOUT: CMP (SP)*,(SP)+ IRESET THE STACK FROM TIMEOUT
565 IKT11 NOT PRESENT, THEREFORE
566 IONLY GO BELOW 20K
567 002326 012637 000004 60: MOV (SP)*,@#4 IRESTORE CONTENTS OF LOC. 4
568 002332 005077 000006 CLR @#6 IRESTORE CONTENTS OF LOC. 6
569 002336 004337 011500 JSR R3,@#INITIALIZE ISET UP TO BEGIN TESTING
570 002340 010703 177040 MOV @#MPAD,R3 ISET UP FOR MEMORY TABLE CREATION
571 002346 010702 176764 MOV @#REGID,R2 ISET UP FOR PARITY TABLE CREATION
572 002352 010700 177040 MOV @#SETAD,R0 ISET UP FOR OFFSET TABLE CREATION
573 ITHIS TABLE ONLY HAS EFFECT IF
574 IMEMORY MGMT IS TURNED ON
575 002356 010705 177064 MOV @#NTERAD,R5 ISET UP FOR INTERLEAVE TABLE
576 ICREATION (8 - WAY INTERLEAVE
577 ICAPABILITY EXISTS)
578
579 *****
580 ILET'S DETERMINE IF SEVERAL REGISTERS EXIST, FOR EXAMPLE,
581 I
582 I 172100 GOVERNING CORE MEMORY 0 = 8K
583 I 172102 GOVERNING NOS MEMORY 8 = 16K
584 I 172112 GOVERNING CORE MEMORY 40 = 56K
585 I
586 IIF WE WANT TO PRESELECT ONE OF THEM OR CREATE A TABLE OF ALL THOSE
587 IAVAILABLE AND CARRY ON TESTING FROM THE TABLE
588 I
589 INOTE: SEE DOCUMENT CONCERNING TABLE APPEARANCES AS A
590 IFUNCTION OF MEMORY MANAGEMENT (KY11 OPTION) BEING
591 IENABLED OR DISABLED DURING PROGRAM EXECUTION
592 I
593 *****

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593 002362 012737 010000 177570 BIT @#BIT12,@#BWR IDOES THE USER WISH TO SELECT THE
594 IREGISTER?
595 002370 001445 BEQ FINDONE IBRANCH IF NO
596 002372 HSGTYPE:
597 002372 104000 002000 TYPE ,,+4 ITYPE ASCII STRING
598 002376 000433 BR @#4 IGET OVER THE ASCII
599 I
600 002466 68: I.ASCIZ <15><12>"TYPE THE REGISTER YOU WANT & HIT CARRIAGE RETURN "
601 002466 104006 001340 ACCEPT,@#REG0 IPICK UP THE DESIRED REGISTER
602 IFROM THE TELETYPE AND STORE
603 IIN FIRST TABLE LOCATION
604 002472 005237 001630 INC @#USERTYPE ISET FLAG INDICATING USER SELECTION
605 002476 013746 000004 MOV @#4,@-(SP) ISAVE CONTENTS OF LOC. 4
606 002502 000404 BR @#NEXT1 ISKIP THE NEXT INSTRUCTIONS
607 002504 012712 172100 FINDONE: MOV @#172100,(R2) IMOVE 1ST POSSIBLE PARITY
608 IREGISTER INTO @#REG0
609 002510 013746 000004 MOV @#4,@-(SP) IPUSH CONTENTS OF LOC.4 ONTO STACK
610 002514 012704 001644 NEXT1: MOV @#INTERTABLE,R4 IINITIALIZE INTERLEAVE TABLE
611 IPOINTER
612 002520 005737 002304 TST @#SKT11 IKT11 ARE YOU THERE?
613 002524 001402 BEQ 55 IBRANCH IF NO
614 002526 005077 175500 CLR @#KPAR1 IRESET PAGE 1 ADDRESS REGISTER
615 IBEFORE TESTING NEXT PARITY
616 ICONTROL REGISTER
617 002532 012737 003144 58: MOV @#NOREG,@#4 ISET PARTLY TIMEOUT VECTOR SERVICE ADDRESS
618 002540 022712 172136 000004 CMP @#172136,(R2) IIS THE ADDRESS IN BOUNDS?
619 002544 100002 BPL 125 IBRANCH IF YES
620 002546 000137 003172 14: JMP @#NOREG IOTHERWISE - TERMINATE TABLE
621 002552 005777 000000 TST @#(R2) IYES - IS THIS REGISTER PRESENT?
622 002556 004737 003222 JSR R7,@#PARTY ICHECK OUT FOR FATAL ERRORS
623
624 I
625 IWE HAVE CHECKED OUT THE REGISTER AND FOUND IT TO BE WORKING PROPERLY
626 INOW WE WILL FIND ITS ASSOCIATED PARITY MEMORY, IF IT EXISTS!!
627 I
628 I
629 002562 012737 003112 000004 MOV @#PARCORE,@#4 ISET MEMORY TIMEOUT VECTOR
630 ISERVICE ADDRESS
631 002570 012737 013700 001640 MOV @#13700,@#MEMAD ISET UP A STARTING ADDRESS
632 002576 011437 001632 MOV (R4),@#BLKCNT ISET A COUNTER FOR CONSECUTIVE
633 ILOCATION CHECKS TO COVER MEMORY
634 IINTERLEAVING
635 002602 005737 002304 TST @#SKT11 I SHOULD I LOOK ABOVE 20K?
636 002606 100013 IS IBRANCH IF NO
637 002610 002737 010000 001640 ADD @#10000,@#MEMAD ISTEP UP TO A PAGE 1 BASE ADDRESS
638 IIF MEMORY MANAGEMENT TURNED ON
639 002616 013737 021640 001634 MOV @#MEMAD,@#RESTOREBASE ISAVE PAGE 1 BASE ADDRESS
640 002624 002710 000140 22: ADD @#140,(R0) ISET UP AN OFFSET FOR KPAR1
641 002630 011077 175376 MOV (R0),@#KPAR1 ISET OFFSET IN PAGE 1 REGISTER
642 002634 000406 BR @#S ISKIP NEXT 2 INSTRUCTIONS
643 002636 002737 000000 001640 13: ADD @#320,@#MEMAD ISTEP UP TO NEXT BANK
644 002644 013737 001640 001634 MOV @#MEMAD,@#RESTOREBASE ISAVE INITIAL MEMORY ADDRESS
645 002652 005777 176762 TST @#MEMAD IIS THIS MEMORY AVAILABLE?
646 002656 013713 001640 MOV @#MEMAD,(R3) IYES - STORE THIS MEMORY LOCATION

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647	002040	004137	003420	JSR	R7,PARCORT	INOW LET'S SEE IF IT'S PARITY
648						MEMORY CORRESPONDING TO THE
649						PARITY REGISTER BEING FOUND
650	002044	005737	003304	TST	PARCORT	OK!! ARE YOU THERE?
651	002048	005737	003304	BPL	48	BRANCH IF NO
652	002052	005537	001432	DEC	PARCORT	DECREASE CONSECUTIVE
653						LOCATION COUNTER
654	002700	005737	001632	TST	PARCORT	ARE WE DONE CHECKING
655						CONSECUTIVE LOCATIONS?
656	002704	001404		BEG	68	BRANCH IF YES
657	002708	002737	000002	ADD	02,PARCORT	STEP UP 1 LOCATION
658	002712	000756		BR	98	GO BACK TO TEST WITH THIS
659						LOCATION
660	002716	012737	001634	MOV	PARSTOREBASE,PARCORT	RESTORE PAGE 1 BASE ADDRESS
661						BEFORE GOING BACK TO INCREASE
662						OFFSET
663	002720	002704	000004	ADD	04,R4	STEP TABLE POINTER UP FOR
664						NEXT VALUE OF CONSECUTIVE
665						LOCATION TO BE CHECKED
666	002724	005714		TST	(R4)	ARE THERE ANY MORE?
667	002728	001404		BEG	104	BRANCH IF NO
668	002732	011437	001632	MOV	(R4),PARCORT	STORE THIS VALUE OF CONSECUTIVE
669						LOCATION CHECKS
670	002736	005037	001636	CLR	PARLEAFONT	CLEAR INTERLEAVE VALUE HOLDER
671						BEFORE RETESTING
672	002740	000742		BR	98	GO BACK TO TEST WITH THIS VALUE
673						OF CONSECUTIVE LOCATIONS
674	002744	012704	001644	MOV	INTERTABLE,R4	INITIALIZE INTERLEAVE TABLE
675						POINTER
676	002748	011437	001632	MOV	(R4),PARCORT	RESET THE CONSECUTIVE
677						LOCATION COUNTER
678	002752	005037	001636	CLR	PARLEAFONT	CLEAR INTERLEAVE VALUE HOLDER
679						BEFORE RETESTING
680	002756	000720		BR	28	GO BACK TO INCREASE OFFSET
681						AND TEST
682	002760	002737	157700	CMP	0157700,PARCORT	ARE WE UP TO 26K YET?
683	002764	001404		BEG	88	BRANCH IF YES
684	002768	005537	001632	DEC	PARCORT	DECREASE CONSECUTIVE
685						LOCATION COUNTER
686	003000	005737	001632	TST	PARCORT	ARE WE DONE CHECKING CONSEC-
687						UTIVE LOCATIONS?
688	003004	001404		BEG	76	BRANCH IF YES
689	003008	002737	000002	ADD	02,PARCORT	STEP UP 1 LOCATION
690	003012	000716		BR	98	GO BACK TO TEST WITH THIS
691						LOCATION
692	003016	012737	001634	MOV	PARSTOREBASE,PARCORT	RESTORE INITIAL MEMORY
693						ADDRESS BEFORE GOING BACK TO
694						STEP UP TO NEXT BANK
695	003020	002704	000004	ADD	04,R4	STEP TABLE POINTER UP FOR
696						NEXT VALUE OF CONSECUTIVE
697						LOCATIONS TO BE CHECKED
698	003024	005714		TST	(R4)	ARE THERE ANY MORE?
699	003028	001404		BEG	112	BRANCH IF NO
700	003032	011437	001632	MOV	(R4),PARCORT	STORE THIS VALUE OF

701	003036	005037	001636	CLR	PARLEAFONT	CONSECUTIVE LOCATION CHECKS
702						CLEAR INTERLEAVE VALUE HOLDER
703	003040	000702		BR	98	BEFORE RETESTING
704						GO BACK TO STEP UP TO THE
705	003044	012704	001644	MOV	INTERTABLE,R4	NEXT BANK TO CONDUCT TESTING
706						INITIALIZE INTERLEAVE
707	003048	011437	001632	MOV	(R4),PARCORT	TABLE POINTER
708						RESET THE CONSECUTIVE
709	003052	005037	001636	CLR	PARLEAFONT	LOCATION COUNTER
710						CLEAR INTERLEAVE VALUE HOLDER
711	003056	000665		BR	18	BEFORE RETESTING
712						GO BACK TO STEP UP TO NEXT
713	003060	011237	001622	MOV	(R2),PARPARITY	BANK TO CONDUCT TESTING
714						STORE THE BAD REGISTER WITH
715	003064	104004		HLT	04	NO PARITY MEMORY
716						NO PARITY MEMORY FOUND
717	003068	005737	001636	TST	PARUSERTYPE	BELOW 28K!!!!!!
718	003072	001404		BEG	38	DID USER SELECT REGISTER?
719	003076	000137	002372	JMP	PARMSGTYP	BRANCH IF NO
720						GO BACK TO RETYPE MESSAGE FOR
721	003104	002712	000002	ADD	02,(R2)	USER RESPONSE
722						PLACE NEXT POSSIBLE REGISTER
723	003108	000601		BR	NEXT1	INTO SAME TABLE LOCATION
724	003112	002626		CMP	(SP)+,(SP)+	GO BACK TO TEST THIS REGISTER
725	003116	011237	001622	MOV	(R2),PARPARITY	RESET STACK FROM MEMORY TIMEOUT
726						STORE THE REGISTER THAT EN-
727						OUNTERED A POSSIBLE HOLE IN
728						MEMORY
729	003120	104007		HLT	07	IS A POSSIBLE HOLE IN MEMORY EXISTS
730						WITH NO PARITY BELOW IT!!!!!!
731	003124	005737	001636	TST	PARUSERTYPE	DID USER SELECT REGISTER?
732	003128	001404		BEG	48	BRANCH IF NO
733	003132	000137	002372	JMP	PARMSGTYP	GO BACK TO RETYPE MESSAGE FOR
734						USER RESPONSE
735	003136	002712	000002	ADD	02,(R2)	PLACE NEXT POSSIBLE REGISTER
736						INTO SAME TABLE LOCATION
737	003140	000137	002514	JMP	PARNEXT1	GO BACK TO TEST THIS REGISTER
738	003144	002626		CMP	(SP)+,(SP)+	RESET STACK FROM REGISTER TIMEOUT
739	003148	005737	001636	TST	PARUSERTYPE	DID THE USER SELECT THE REGISTER?
740	003152	001404		BEG	18	BRANCH IF NO
741	003156	104006		HLT	06	YES = USER SELECTED REGISTER NOT
742						PRESENT ON SYSTEM
743	003160	000137	002372	JMP	PARMSGTYP	GO BACK TO RETYPE MESSAGE
744	003164	002712	000002	ADD	02,(R2)	STEP UP TO NEXT PARITY REGISTER
745						AT SAME TABLE LOCATION
746	003168	000137	002514	JMP	PARNEXT1	PREVIOUS PARITY REGISTER NOT
747						PRESENT - SEE IF THE NEXT ONE IS
748	003172	012637	000004	MOV	(SP)+,004	RESTORE CONTENTS OF LOC. 4
749	003176	005012		CLR	(R2)	ALL DONE TABLE CREATION
750						END IT WITH A 'G'
751	003200	000137	002620	JMP	PARSTART	START RUNNING PROGRAM WITH
752						TABLE CONTENTS
753						*****
754						*****

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755 /THE FOLLOWING ROUTINE WILL CREATE A 2 LOCATION MEMORY MAP AT
756 /THE HIGH END OF A 1K CORE SECTION. THIS 2 LOCATION MAP WILL
757 /INITIALLY BE USED TO DETERMINE WHERE/IF PARITY MEMORY
758 /PRESIDES AND LATER FOR SUBSEQUENT PROGRAM TESTING OF A REGISTER
759 /
760 /*****
761 003204 102700 000376 COMPUT: SUB    0376,RO      /DROP DOWN SO AS NOT TO
762 /                          /DESTROY ABS LOADER
763 003210 010001      NOV    R0,R1      /R1 CONTAINS BEGINNING ADDRESS
764 /                          /OF MEMORY MAP
765 003212 000000      CMP    R0,(R0)+  /STEP R0 TO NEXT ADDRESS
766 003214 010011      MOV    R0,R1      /1ST MEMORY LOCATION
767 003216 011110      MOV    R01,R00     /2ND MEMORY LOCATION
768 003220 000203      RTS    R3        /RETURN TO TEST A DATA
769 /                          /WITH CONTENTS OF THESE 2 LOCS.
770 /
771 /*****
772 /
773 /THIS ROUTINE WILL CHECK IF THE PARITY REGISTER IS STATICALLY IN
774 /GOOD OPERATION FOR TESTING TO BE CONDUCTED
775 /
776 /*****
777 003222 011267 176374 PARTST: MOV    (R0),PARITY  /GET PARITY REGISTER TO BE USED
778 /                          /
779 /TEST 1 SET BITS (USED) OF PARITY REGISTER
780 /*****
781 003226 000004      TST1: SCOPE
782 003230 002777 000001 176364 BIC    @BIT0,@PARITY
783 003234 002777 000001 176356 BIT    @BIT0,@PARITY      /DID IT SET?
784 003244 001001      BNE    +4          /YES
785 003246 104002      HLT    +2          /NO - FATAL ERROR TO PROGRAM!
786 /*****
787 /TEST 2 CLEAR BITS (USED) OF PARITY REGISTER
788 /*****
789 003250 000004      TST2: SCOPE
790 003252 002777 000001 176342 BIC    @BIT0,@PARITY
791 003254 002777 000001 176334 BIT    @BIT0,@PARITY      /DID IT CLEAR?
792 003256 001001      BEO    +4          /YES
793 003270 104002      HLT    +2          /NO - FATAL ERROR TO PROGRAM!
794 /*****
795 /TEST 3 SET AND CLEAR BITS (USED) OF PARITY REGISTER
796 /*****
797 003272 000004      TST3: SCOPE
798 003274 002777 000004 176320 BIC    @BIT2,@PARITY
799 003302 002777 000004 176312 BIT    @BIT2,@PARITY      /DID IT SET?
800 003310 001001      BNE    +4          /YES
801 003312 104002      HLT    +2          /NO - FATAL ERROR TO PROGRAM!
802 003314 002777 000004 176300 BIC    @BIT2,@PARITY
803 003322 002777 000004 176272 BIT    @BIT2,@PARITY      /DID IT CLEAR?
804 003330 001001      BEO    +4          /YES
805 003332 104002      HLT    +2          /NO - FATAL ERROR TO PROGRAM!
806 /*****
807 /TEST 4 TEST RESET ON BITS 0, 2 AND 15
808 /*****

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809 003334 000004      TST4: SCOPE
810 003336 005737 002304      TST    @BKT11
811 003342 100015      BHI    WHICH1      /BKT11 ON?
812 /                          /BRANCH IF YES AND DON'T DO
813 /                          /THIS TEST BECAUSE THE 'RESET'
814 /                          /WILL Clobber SEGMENTATION
815 003344 002777 100005 176250 BIC    @100005,@PARITY
816 003352 000005      RESET
817 003354 002777 100005 176240 BIT    @100005,@PARITY
818 003362 001004      BEQ    +12          /DID THEY CLEAR?
819 003364 002777 100005 176230 BIC    @100005,@PARITY
820 /                          /PRECAUTION
821 /                          /RESET DOESN'T WORK
822 /*****
823 /TEST 5 WHICH OPTION IS ABOUT TO BE TESTED
824 /*****
825 003374 000004      TST5: SCOPE
826 003376 002777 007740 176216 WHICH1: BIC    @7740,@PARITY
827 /                          /IS AN OLD MS11 OPTION
828 /                          /WITH NO ADDRESS BITS
829 003404 002777 007740 176210 BIT    @7740,@PARITY
830 003412 001002      BEO    15          /ABOUT TO BE TESTED?
831 003414 000004      SCOPE
832 003416 000207      RTS    R7          /ADDRESS BITS ABLE TO BE SET?
833 003420 002237 001626      IS1: INC    @MSREGFLAG
834 /                          /BRANCH IF NO INDICATING MS11
835 003424 000004      SCOPE
836 003426 000207      RTS    R7          /RETURN TO NORMAL FLOW
837 /                          /RETURN TO NORMAL FLOW
838 /*****
839 /
840 /THE FOLLOWING ROUTINE WILL TAKE EACH 1K BANK OF MEMORY
841 /THAT IS AVAILABLE AND PERFORM A DATA IN IT
842 /TO DETERMINE IF PARITY EXISTS THERE. THIS ROUTINE IS
843 /ONLY USED DURING TABLE CREATION
844 /
845 /*****
846 003430 010546      ABORT: MOV    R5,*(SP)    /SAVE R5 CONTENTS ON STACK
847 003432 010046      MOV    R0,*(SP)    /SAVE R0 CONTENTS ON STACK
848 003434 011300      MOV    (R5),R0     /GET THE MEMORY LOCATION
849 /                          /JUST DETERMINED
850 003436 004337 003204      JSR    R3,@R0COMP
851 /                          /COMPUTE AN AREA IN THIS BANK
852 /                          /FOR DETERMINING PARITY MEMORY
853 003442 011267 176150      MOV    (R2),PARITY
854 /                          /GET THE PARITY REGISTER JUST
855 /                          /FOUND AND TEST WITH IT
856 /*****
857 /
858 /TEST A DATA IN THIS BANK
859 /*****
860 003446 012705 011450      /1/45 *** ROM STATE 221 ***
861 003452 004015      /
862 003454 003300      /
863 /
864 /
865 /
866 003466 012705 011450      /11/40 *** ROM STATE 207 ***
867 003452 004015      MOV    @VECSY,R5    /SET UP SERVICE ROUTINE ADDRESS
868 003454 003300      JSR    R0,(R5)     /SET UP PARITY VECTOR SERVICE
869 /                          /ROUTINE ADDRESS

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971          002676 000037 003200      JSR    R3,PCOCHKPUT      IWRITE THE PARITY REGISTER
972          003702 010137 001476      MOV    R1,PCNCHSITK      ICOMPUTE AN AREA IN THIS BANK
973                                     IFOR TESTING
974          003706 000737 003376      JSR    PC,PCNHIOM1      ISET UP A NEW STACK POINTER
975                                     IFOR STACK OPERATIONS IN CASE
976                                     IAND PARITY MEMORY RESIDES IN
977                                     ILOWER CK
978          003712 012725 011450      MOV    @VPCSET,R0      IDETERMINE IF WE ARE ABOUT TO
979                                     ITEST AN OLD NOS DESIGN!
980                                     ISET UP SERVICE ROUTINE ADDRESS
981          003716 000004      TSTB1 SCOPE
982          11/45 *** ROM STATE 221 ***
983          11/40 *** ROM STATE 207 ***
984          003720 000015      JSR    R0,(R5)          ISET UP PARITY VECTOR SERVICE
985          003722 003752      A      IROUTINE ADDRESS
986          003724 011102      MOV    @R1,R0          ISET UP FUR DATO
987          003726 010010      MOV    R0,R0          IDO THE DATO
988          003730 012737 003740 001332      MOV    @+10,@#S4DDAT      ISTORE THE PC THAT SHOULD
989                                     IBE PUSHED ON THE STACK
990                                     IIF A PARITY ABORT OCCURS
991          003736 010030      MOV    R0,@2(R0)      IDO A DATI
992          003740 042777 000004 175650      BIC    @BIT2,@PARITY      IWRITE NORMAL FOR EMT CALL
993          003746 104001      HLT    +1              IDIDN'T APORT
994          003750 000010      BR     +22             IGO TO NEXT TEST
995          003752 042777 000005 175642 A1    BIC    @BIT2|BIT0,@PARITY IWRITE NORMAL AND DISABLE
996          003760 004037 011550      JSR    R0,@#CHECKLOC      ICHECK FOR GOOD APORT
997          003764 104003      HLT    +3              IABORTED INCORRECTLY
998          003766 012706 001100      MOV    @STACK,SP        IRESET THE STACK
999          003772 000004      TSTB1 SCOPE
1000         11/45 *** ROM STATE 231 ***
1001         11/40 *** ROM STATE 207 ***
1002         003774 000015      JSR    R0,(R5)          ISET UP PARITY VECTOR SERVICE
1003         003776 104026      A      IROUTINE ADDRESS
1004         004000 011100      MOV    @R1,R0          ISET UP FUR DATO
1005         004002 010020      MOV    R0,(R0)        IDO THE DATO
1006         004004 012737 000014 001332      MOV    @+10,@#S4DDAT      ISTORE THE PC THAT SHOULD
1007                                     IBE PUSHED ON THE STACK
1008                                     IIF A PARITY ABORT OCCURS
1009         004012 010050      MOV    R0,@2(R0)      IDO A DATI
1010         004014 042777 000004 175600      BIC    @BIT2,@PARITY      IWRITE NORMAL FOR EMT CALL
1011         004022 104001      HLT    +1              IDIDN'T APORT
1012         004024 000010      BR     +22             IGO TO NEXT TEST
1013         004026 042777 000005 175566 A01    BIC    @BIT2|BIT0,@PARITY IWRITE NORMAL AND DISABLE
1014         004034 004037 011550      JSR    R0,@#CHECKLOC      ICHECK FOR GOOD APORT
1015         004036 104003      HLT    +3              IABORTED INCORRECTLY
1016         004038 012706 001100      MOV    @STACK,SP        IRESET THE STACK
    
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1025          004046 000004      TSTB1 SCOPE
1026          11/45 *** ROM STATE 27 ***
1027          11/40 *** ROM STATE 206 ***
1028         004050 000015      JSR    R0,(R5)          ISET UP PARITY VECTOR SERVICE
1029         004052 004104      A1      IROUTINE ADDRESS
1030         004054 011100      MOV    @R1,R0          ISET UP FUR DATO
1031         004056 010010      MOV    R0,R0          IDO THE DATO
1032         004060 012737 004070 001332      MOV    @+10,@#S4DDAT      ISTORE THE PC THAT SHOULD
1033                                     IBE PUSHED ON THE STACK
1034                                     IIF A PARITY ABORT OCCURS
1035         004066 011060 177776      MOV    @R0,@2(R0)      IDO A DATI
1036         004072 042777 000004 175522      BIC    @BIT2,@PARITY      IWRITE NORMAL FOR EMT CALL
1037         004100 104001      HLT    +1              IDIDN'T APORT
1038         004102 000010      BR     +22             IGO TO NEXT TEST
1039         004104 042777 000005 175510 A11    BIC    @BIT2|BIT0,@PARITY IWRITE NORMAL AND DISABLE
1040         004112 004037 011550      JSR    R0,@#CHECKLOC      ICHECK FOR GOOD APORT
1041         004116 104003      HLT    +3              IABORTED INCORRECTLY
1042         004120 012706 001100      MOV    @STACK,SP        IRESET THE STACK
1043          004124 000004      TSTB11 SCOPE
1044          11/45 *** ROM STATE 231 ***
1045          11/40 *** ROM STATE 207 ***
1046         004126 000015      JSR    R0,(R5)          ISET UP PARITY VECTOR SERVICE
1047         004130 004162      A2      IROUTINE ADDRESS
1048         004132 011100      MOV    @R1,R0          ISET UP FUR A DATO
1049         004134 010020      MOV    R0,(R0)        IDO THE DATO
1050         004136 012737 004150 001332      MOV    @+12,@#S4DDAT      ISTORE THE PC THAT SHOULD
1051                                     IBE PUSHED ON THE STACK
1052                                     IIF A PARITY ABORT OCCURS
1053         004144 010070 177776      MOV    R0,@2(R0)      IDO A DATI
1054         004150 042777 000004 175444      BIC    @BIT2,@PARITY      IWRITE NORMAL FOR EMT CALL
1055         004156 104001      HLT    +1              IDIDN'T APORT
1056         004160 000010      BR     +22             IGO TO NEXT TEST
1057         004162 042777 000005 175032 A21    BIC    @BIT2|BIT0,@PARITY IWRITE NORMAL AND DISABLE
1058         004170 004037 011550      JSR    R0,@#CHECKLOC      ICHECK FOR GOOD APORT
1059         004174 104003      HLT    +3              IABORTED INCORRECTLY
1060         004176 012706 001100      MOV    @STACK,SP        IRESET THE STACK
1061          004202 000004      TSTB12 SCOPE
1062          11/45 *** ROM STATE 175 ***
1063          11/40 *** ROM STATE 267 ***
1064         004204 000015      JSR    R0,(R5)          ISET UP PARITY VECTOR SERVICE
1065         004206 004236      B      IROUTINE ADDRESS
1066         004210 011100      MOV    @R1,R0          ISET UP FUR DATO
    
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1079 004210 010010      MOV      R0,R00      /DO THE DATO
1080 004214 012737 004220 001330      MOV      R,+10,0050DDAT /STORE THE PC THAT SHOULD
1081                                     /BE PUSHED ON THE STACK
1082                                     /IF A PARITY ABORT OCCURS
1083 004220 000000      CMP      R0,(R0)+    /DO A DATAP, DATI
1084 004224 042777 000004 175070      BIC     #BIT2,0PARITY /WRITE NORMAL FOR EMT CALL
1085 004228 100001      HLT     +1          /DIDN'T ABORT
1086 004234 000410      BR     +22         /GO TO NEXT TEST
1087 004236 042777 000005 175056 B1     BIC     #BIT2IBIT0,0PARITY /WRITE NORMAL AND DISABLE
1088 004244 000037 011550      JSR     R0,00CHECKLOC /CHECK FOR GOOD ABORT
1089 004250 100003      HLT     +3          /ABORTED INCORRECTLY
1090 004252 012706 001100      MOV     #STACK,SP   /RESET THE STACK
/*****
/TEST 13      TEST (DATA) SMO,DM0 CMP INSTRUCTION
/*****
TST13: SCOPE      11/45 *** ROM STATE 177 ***
/
/
/      11/40 *** ROM STATE 267 ***
1098 004260 000015      JSR     R0,(R5)     /SET UP PARITY VECTOR SERVICE
1099 004264 004318      BR     /ROUTINE ADDRESS
1100 004268 011100      MOV     #R1,R0     /SET UP FOR DATO
1101 004274 000000      MOV     R0,(R0)+   /DO THE DATO
1102 004278 012737 004300 001332      MOV     R,+10,0050DDAT /STORE THE PC THAT SHOULD
1103                                     /BE PUSHED ON THE STACK
1104                                     /IF A PARITY ABORT OCCURS
1105 004276 000000      CMP     R0,(R0)    /DO A DATAP, DATI
1106 004300 042777 000004 175310      BIC     #BIT2,0PARITY /WRITE NORMAL FOR EMT CALL
1107 004304 100001      HLT     +1          /DIDN'T ABORT
1108 004310 000410      BR     +22         /GO TO NEXT TEST
1109 004312 042777 000005 175302 B0:     BIC     #BIT2IBIT0,0PARITY /WRITE NORMAL AND DISABLE
1110 004320 000037 011550      JSR     R0,00CHECKLOC /CHECK FOR GOOD ABORT
1111 004324 100003      HLT     +3          /ABORTED INCORRECTLY
1112 004326 012706 001100      MOV     #STACK,SP   /RESET THE STACK
/*****
/TEST 14      TEST (DATA) SMO,DM6 CMP INSTRUCTION
/*****
TST14: SCOPE      11/45 *** ROM STATE 177 ***
/
/
/      11/40 *** ROM STATE 267 ***
1120 004330 000015      JSR     R0,(R5)     /SET UP PARITY VECTOR SERVICE
1121 004334 004370      BR     /ROUTINE ADDRESS
1122 004338 011100      MOV     #R1,R0     /SET UP FOR DATO
1123 004342 000000      MOV     R0,(R0)+   /DO THE DATO
1124 004344 012737 004306 001332      MOV     R,+12,0050DDAT /STORE THE PC THAT SHOULD
1125                                     /BE PUSHED ON THE STACK
1126                                     /IF A PARITY ABORT OCCURS
1127 004352 000000 177776      CMP     R0,-2(R0)  /DO A DATAP, DATI
1128 004356 042777 000004 175236      BIC     #BIT2,0PARITY /WRITE NORMAL FOR EMT CALL
1129 004364 100001      HLT     +1          /DIDN'T ABORT
1130 004366 000410      BR     +22         /GO TO NEXT TEST
1131 004370 042777 000005 175224 B1:     BIC     #BIT2IBIT0,0PARITY /WRITE NORMAL AND DISABLE
1132 004374 000037 011550      JSR     R0,00CHECKLOC /CHECK FOR GOOD ABORT

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1133 004402 100003      HLT     +3          /ABORTED INCORRECTLY
1134 004404 012706 001100      MOV     #STACK,SP   /RESET THE STACK
/*****
/TEST 15      TEST (DATA) SMO,DM1 CMP INSTRUCTION
/*****
TST15: SCOPE      11/45 *** ROM STATE 175 ***
/
/
/      11/40 *** ROM STATE 267 ***
1142 004412 000015      JSR     R0,(R5)     /SET UP PARITY VECTOR SERVICE
1143 004416 004444      BR     /ROUTINE ADDRESS
1144 004420 011100      MOV     #R1,R0     /SET UP FOR DATO
1145 004424 000000      MOV     R0,R0      /DO THE DATO
1146 004428 012737 004432 001332      MOV     R,+10,0050DDAT /STORE THE PC THAT SHOULD
1147                                     /BE PUSHED ON THE STACK
1148                                     /IF A PARITY ABORT OCCURS
1149 004430 000010      CMP     R0,0R0     /DO A DATAP, DATI
1150 004432 042777 000004 175162      BIC     #BIT2,0PARITY /WRITE NORMAL FOR EMT CALL
1151 004440 100001      HLT     +1          /DIDN'T ABORT
1152 004442 000410      BR     +22         /GO TO NEXT TEST
1153 004444 042777 000005 175150 B2:     BIC     #BIT2IBIT0,0PARITY /WRITE NORMAL AND DISABLE
1154 004452 000037 011550      JSR     R0,00CHECKLOC /CHECK FOR GOOD ABORT
1155 004456 100003      HLT     +3          /ABORTED INCORRECTLY
1156 004460 012706 001100      MOV     #STACK,SP   /RESET THE STACK
/*****
/TEST 16      TEST (DATA) SMO,DM3 CMP INSTRUCTION
/*****
TST16: SCOPE      11/45 *** ROM STATE 177 ***
/
/
/      11/40 *** ROM STATE 267 ***
1164 004466 000015      JSR     R0,(R5)     /SET UP PARITY VECTOR SERVICE
1165 004470 004536      BR     /ROUTINE ADDRESS
1166 004472 011100      MOV     #R1,R0     /SET UP FOR DATO
1167 004474 010020      MOV     R0,(R0)+   /DO THE DATO
1168 004476 042777 000004 175116      BIC     #BIT2,0PARITY /WRITE NORMAL
1169 004480 011110      MOV     #R1,0R0    /WRITE ADDRESS NORMAL (DATI)
1170 004482 042777 000004 175106      BIC     #BIT2,0PARITY /WRITE OTHER PARITY
1171 004484 012737 004524 001332      MOV     R,+10,0050DDAT /STORE THE PC THAT SHOULD
1172                                     /BE PUSHED ON THE STACK
1173                                     /IF A PARITY ABORT OCCURS
1174 004522 000030      CMP     R0,(R0)+   /DO A DATAP, DATI
1175 004524 042777 000004 175070      BIC     #BIT2,0PARITY /WRITE NORMAL FOR EMT CALL
1176 004532 100001      HLT     +1          /DIDN'T ABORT
1177 004534 000410      BR     +22         /GO TO NEXT TEST
1178 004536 042777 000005 175056 B3:     BIC     #BIT2IBIT0,0PARITY /WRITE NORMAL AND DISABLE
1179 004544 000037 011550      JSR     R0,00CHECKLOC /CHECK FOR GOOD ABORT
1180 004550 100003      HLT     +3          /ABORTED INCORRECTLY
1181 004552 012706 001100      MOV     #STACK,SP   /RESET THE STACK
/*****
/TEST 17      TEST (ADDRESS) SMO,DM5 CMP INSTRUCTION
/*****
TST17: SCOPE      11/45 *** ROM STATE 221 ***
/

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```
1167  
1168  
1169 004560 004015 JSR R0,(R5) ISET UP PARITY VECTOR SERVICE  
1190 004560 004018 B4 IROUTINE ADDRESS  
1191 004560 011100 MOV R01,R0 ISET UP FOR A DAYO  
1192 004566 010010 MOV R0,R0 IDO THE DATO  
1193 004570 012737 004600 001032 MOV R,+10,00S0DAT ISTORE THE PC THAT SHOULD  
1194 IBE PUSHED ON THE STACK  
1195 IIF A PARITY ABORT OCCURS  
1196 004576 040030 CMP R0,(R0)+ IDO A DATI  
1197 004600 042777 000004 175014 BIC 0BIT2,0PARITY IWRITE NORMAL FOR EMT CALL  
1198 004606 104001 HLT +1 I'DIDN'T ABORT  
1199 004610 000410 BR +22 IGO TO NEXT TEST  
1200 004612 042777 000005 175002 601 BIC 0BIT2|BIT0,0PARITY IWRITE NORMAL AND DISABLE  
1201 004620 004037 011550 JSR R0,00CHECKLOC ICHECK FOR GOOD ABORT  
1202 004624 104003 HLT +3 IABORTED INCORRECTLY  
1203 004626 012706 001100 MOV 0STACK,SP IRESET THE STACK  
1204 I*****  
1205 ITEST 20 TEST DATA (DATA) SH0,DMS CMP INSTRUCTION  
1206 I*****  
1207 004632 000004 T0T20: SCOPE  
1208 I  
1209 I 11/05 *** ROM STATE 177 ***  
1210 I  
1211 004634 004015 JSR R0,(R5) ISET UP PARITY VECTOR SERVICE  
1212 004636 004018 B5 IROUTINE ADDRESS  
1213 004640 011100 MOV R01,R0 ISET UP FOR A DAYO  
1214 004642 042777 000004 174752 BIC 0BIT2,0PARITY IWRITE NORMAL  
1215 004650 010000 177776 MOV R0,(R0) IDO THE DATO  
1216 004654 042777 000004 174740 B10 IWRITE OTHER PARITY  
1217 004662 011110 MOV R01,R0 IDO THE DATO  
1218 004664 012737 004674 001332 MOV R,+10,00S0DAT ISTORE THE PC THAT SHOULD  
1219 IBE PUSHED ON THE STACK  
1220 IIF A PARITY ABORT OCCURS  
1221 004672 040030 CMP R0,(R0) IDO A DATI, DAYIP  
1222 004674 042777 000004 174720 BIC 0BIT2,0PARITY IWRITE NORMAL FOR EMT CALL  
1223 004702 104001 HLT +1 I'DIDN'T ABORT  
1224 004704 000410 BR +22 IGO TO NEXT TEST  
1225 004706 042777 000005 174706 601 BIC 0BIT2|BIT0,0PARITY IWRITE NORMAL AND DISABLE  
1226 004714 004037 011550 JSR R0,00CHECKLOC ICHECK FOR GOOD ABORT  
1227 004720 104003 HLT +3 IABORTED INCORRECTLY  
1228 004722 012706 001100 MOV 0STACK,SP IRESET THE STACK  
1229 I*****  
1230 ITEST 21 TEST (ADDRESS) SH0,DMS CMP INSTRUCTION  
1231 I*****  
1232 004726 000004 T0T21: SCOPE  
1233 I  
1234 I 11/45 *** ROM STATE 231 ***  
1235 I  
1236 004730 004015 JSR R0,(R5) ISET UP PARITY VECTOR SERVICE  
1237 004732 004018 B6 IROUTINE ADDRESS  
1238 004734 011100 MOV R01,R0 ISET UP FOR A DAYO  
1239 004736 010010 MOV R0,R0 IDO THE DATO  
1240 004740 042700 ADD 02,R0
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1241 004744 012737 004754 001332 MOV R,+10,00S0DAT ISTORE THE PC THAT SHOULD  
1242 IBE PUSHED ON THE STACK  
1243 IIF A PARITY ABORT OCCURS  
1244 004752 040030 CMP R0,(R0) IDO A DATI  
1245 004754 042777 000004 174640 BIC 0BIT2,0PARITY IWRITE NORMAL FOR EMT CALL  
1246 004762 104001 HLT +1 I'DIDN'T ABORT  
1247 004764 000410 BR +22 IGO TO NEXT TEST  
1248 004766 042777 000005 174626 601 BIC 0BIT2|BIT0,0PARITY IWRITE NORMAL AND DISABLE  
1249 004774 004037 011550 JSR R0,00CHECKLOC ICHECK FOR GOOD ABORT  
1250 005000 104003 HLT +3 IABORTED INCORRECTLY  
1251 005002 012706 001100 MOV 0STACK,SP IRESET THE STACK  
1252 I*****  
1253 ITEST 22 TEST (DATA) SH2,DMS CMP INSTRUCTION  
1254 I*****  
1255 005006 000004 T0T22: SCOPE  
1256 I  
1257 I 11/45 *** ROM STATE 27 ***  
1258 I  
1259 005010 004015 JSR R0,(R5) ISET UP PARITY VECTOR SERVICE  
1260 005012 005042 C IROUTINE ADDRESS  
1261 005014 011100 MOV R01,R0 ISET UP FOR A DAYO  
1262 005016 010010 MOV R0,R0 IDO THE DATO  
1263 005020 012737 005030 001332 MOV R,+10,00S0DAT ISTORE THE PC THAT SHOULD  
1264 IBE PUSHED ON THE STACK  
1265 IIF A PARITY ABORT OCCURS  
1266 005026 042000 CMP (R0)+,R0 IDO A DATI  
1267 005030 042777 002004 174564 BIC 0BIT2,0PARITY IWRITE NORMAL FOR EMT CALL  
1268 005036 104001 HLT +1 I'DIDN'T ABORT  
1269 005040 000410 BR +22 IGO TO NEXT TEST  
1270 005042 042777 000005 174552 C1 BIC 0BIT2|BIT0,0PARITY IWRITE NORMAL AND DISABLE  
1271 005050 004037 011550 JSR R0,00CHECKLOC ICHECK FOR GOOD ABORT  
1272 005054 104003 HLT +3 IABORTED INCORRECTLY  
1273 005056 012706 001100 MOV 0STACK,SP IRESET THE STACK  
1274 I*****  
1275 ITEST 23 TEST (DATA) SH4,DMS CMP INSTRUCTION  
1276 I*****  
1277 005062 000004 T0T23: SCOPE
```

1276

11/25 **** ROM STATE 27 ****

1279

I

11/28 **** ROM STATE 280 ****

1280

I

JBR R0,(R5)

ISET UP PARITY VECTOR SERVICE

1281 005064 004015

CO

IROUTINE ADDRESS

1282 005066 005116

MOV

ISET UP FOR DATA

1283 005070 011100

MOV

IADD THE DATA

1284 005072 016020

MOV

R0,(R0)*

```
1288 005074 012737 005100 001332 MOV 0,+10,0050DAT STORE THE PC THAT SHOULD  
1289 1746 BE PUSHED ON THE STACK  
1290 1297 IF A PARITY ABORT OCCURS  
1291 1298 DO A DAT  
1292 005102 040000 CMP *(R0),R0 IWRITE NORMAL FOR EMT CALL  
1293 005104 042777 000004 174510 BIC #BIT2,0PARITY IWRITE NORMAL FOR EMT CALL  
1294 005112 104001 HLT +1 IDONT ABORT  
1295 005114 000410 BR +22 IGO TO NEXT TEST  
1296 005116 042777 000004 174476 C4: BIC #BIT2|BIT0,0PARITY IWRITE NORMAL AND DISABLE  
1297 005124 040003 011550 JSR R0,#CHECKLOC ICHECK FOR GOOD ABORT  
1298 005130 104003 HLT +3 IABORTED INCORRECTLY  
1299 005132 012706 001100 MOV #STACK,SP IRESET THE STACK  
1300 *****  
1301 ITEST 20 TEST (DATA) SM3,DMS CMP INSTRUCTION  
1302 *****  
1303 TST20: SCOPE  
1304 I  
1305 11/45 *** ROM STATE 146 ***  
1306 I  
1307 1308 005140 000015 JSR R0,(R5) ISET UP PARITY VECTOR SERVICE  
1309 005142 005210 C1 IROUTINE ADDRESS  
1310 005144 011100 MOV #R1,R0 ISET UP FOR DAT  
1311 005146 010000 MOV #R0,0R0 IDO THE DAT (DATA OTHER PARITY)  
1312 005150 042777 000004 174404 BIC #BIT2,0PARITY IWRITE NORMAL  
1313 005154 011110 MOV #R1,0R0 IDO A DAT (ADDRESS NORMAL)  
1314 005156 042777 000004 174430 BIC #BIT2,0PARITY IWRITE OTHER PARITY  
1315 005160 012737 005176 001332 MOV 0,+10,0050DAT ISTORE THE PC THAT SHOULD  
1316 BE PUSHED ON THE STACK  
1317 IF A PARITY ABORT OCCURS  
1318 DO A DAT  
1319 005170 023000 CMP 0(R0)+,R0 IWRITE NORMAL FOR EMT CALL  
1320 005172 042777 000004 174416 BIC #BIT2,0PARITY IWRITE NORMAL FOR EMT CALL  
1321 005200 104001 HLT +1 IDONT ABORT  
1322 005202 000410 BR +22 IGO TO NEXT TEST  
1323 005210 042777 000004 174404 C1: BIC #BIT2|BIT0,0PARITY IWRITE NORMAL AND DISABLE  
1324 005212 040003 011550 JSR R0,#CHECKLOC ICHECK FOR GOOD ABORT  
1325 005222 104003 HLT +3 IABORTED INCORRECTLY  
1326 005224 012706 001100 MOV #STACK,SP IRESET THE STACK  
1327 *****  
1328 ITEST 25 TEST (DATA) SM5,DMS CMP INSTRUCTION  
1329 *****  
1330 TST25: SCOPE  
1331 I  
1332 11/45 *** ROM STATE 146 ***  
1333 I  
1334 1335 005232 000015 JSR R0,(R5) ISET UP PARITY VECTOR SERVICE  
1336 005234 005304 C2 IROUTINE ADDRESS  
1337 005236 011100 MOV #R1,R0 ISET UP FOR A DAT  
1338 005240 042777 000004 174354 BIC #BIT2,0PARITY IWRITE NORMAL  
1339 005242 010000 MOV #R0,0R0 IDO A DAT (ADDRESS NORMAL)  
1340 005244 042777 000004 174342 BIC #BIT2,0PARITY IWRITE OTHER PARITY  
1341 005246 011110 MOV #R1,0R0 IDO A DAT (DATA OTHER PARITY)  
1342 005248 012737 005272 001332 MOV 0,+10,0050DAT ISTORE THE PC THAT SHOULD  
1343 BE PUSHED ON THE STACK  
1344 IF A PARITY ABORT OCCURS  
1345 DO A DAT  
1346 005270 025000 CMP 0-(R0),R0 IWRITE NORMAL FOR EMT CALL  
1347 *****
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1348 005272 042777 BIC #BIT2,0PARITY IWRITE NORMAL FOR EMT CALL  
1349 005300 104001 HLT +1 IDONT ABORT  
1350 005302 000410 BR +22 IGO TO NEXT TEST  
1351 005304 042777 000004 174310 C4: BIC #BIT2|BIT0,0PARITY IWRITE NORMAL AND DISABLE  
1352 005312 040003 011550 JSR R0,#CHECKLOC ICHECK FOR GOOD ABORT  
1353 005316 104003 HLT +3 IABORTED INCORRECTLY  
1354 005320 012706 001100 MOV #STACK,SP IRESET THE STACK  
1355 *****  
1356 ITEST 26 TEST (DATA) SM1,DMS CMP INSTRUCTION  
1357 *****  
1358 TST26: SCOPE  
1359 I  
1360 11/45 *** ROM STATE 27 ***  
1361 I  
1362 1363 005326 000015 JSR R0,(R5) ISET UP PARITY VECTOR SERVICE  
1364 005330 005360 C5 IROUTINE ADDRESS  
1365 005332 011100 MOV #R1,R0 ISET UP FOR DAT  
1366 005334 010010 MOV #R0,0R0 IDO THE DAT  
1367 005336 012737 005346 001332 MOV 0,+10,0050DAT ISTORE THE PC THAT SHOULD  
1368 BE PUSHED ON THE STACK  
1369 IF A PARITY ABORT OCCURS  
1370 DO A DAT  
1371 005344 021000 CMP 0R0,R0 IWRITE NORMAL FOR EMT CALL  
1372 005346 042777 000004 174246 BIC #BIT2,0PARITY IWRITE NORMAL FOR EMT CALL  
1373 005354 104001 HLT +1 IDONT ABORT  
1374 005356 000410 BR +22 IGO TO NEXT TEST  
1375 005360 042777 000004 174234 C5: BIC #BIT2|BIT0,0PARITY IWRITE NORMAL AND DISABLE  
1376 005366 040003 011550 JSR R0,#CHECKLOC ICHECK FOR GOOD ABORT  
1377 005372 104003 HLT +3 IABORTED INCORRECTLY  
1378 005374 012706 001100 MOV #STACK,SP IRESET THE STACK  
1379 *****  
1380 ITEST 27 TEST (DATA) SM6,DMS CMP INSTRUCTION  
1381 *****  
1382 TST27: SCOPE  
1383 I  
1384 11/45 *** ROM STATE 142 ***  
1385 I  
1386 1387 005402 000015 JSR R0,(R5) ISET UP PARITY VECTOR SERVICE  
1388 005404 005436 C4 IROUTINE ADDRESS  
1389 005406 011100 MOV #R1,R0 ISET UP FOR A DAT  
1390 005410 010020 MOV #R0,(R0)+ IDO THE DAT  
1391 005412 012737 005424 001332 MOV 0,+12,0050DAT ISTORE THE PC THAT SHOULD  
1392 BE PUSHED ON THE STACK  
1393 IF A PARITY ABORT OCCURS  
1394 DO A DAT  
1395 005420 026000 CMP +2(R0),R0 IWRITE NORMAL FOR EMT CALL  
1396 005424 042777 000004 174170 BIC #BIT2,0PARITY IWRITE NORMAL FOR EMT CALL  
1397 005432 104001 HLT +1 IDONT ABORT  
1398 005434 000410 BR +22 IGO TO NEXT TEST  
1399 005436 042777 000004 174156 C4: BIC #BIT2|BIT0,0PARITY IWRITE NORMAL AND DISABLE  
1400 005444 040003 011550 JSR R0,#CHECKLOC ICHECK FOR GOOD ABORT  
1401 005450 104003 HLT +3 IABORTED INCORRECTLY  
1402 005452 012706 001100 MOV #STACK,SP IRESET THE STACK  
1403 *****  
1404 ITEST 30 TEST (ADDRESS) SM7,DMS CMP INSTRUCTION  
1405 *****
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1392 005454 000004          TEST01 SCOPE
1393 11/45 *** ROM STATE 142 ***
1394 11/45 *** ROM STATE 245 ***
1395 JSR  R0,(R0)          ISET UP PARITY VECTOR SERVICE
1396          IROUTINE ADDRESS
1397 005460 000017          MOV  #01,R0          ISET UP FOR A DATA
1398 005462 000010          MOV  #0,(R0)+        IADD THE DATA
1399 005464 011100          MOV  #+12,00540DAT  ISTORE THE PC THAT SHOULD
1400 005466 010000          MOV  #0,00540DAT  IBE PUSHED ON THE STACK
1401 005470 012737 005502 001352          MOV  #0,00540DAT  IIF A PARITY ABORT OCCURS
1402          IDO A DATA
1403          IWRITE NORMAL FOR EMT CALL
1404 005476 001000 177776          CMP  #2(R0),R0      IDIDN'T ABORT
1405 005500 042777 000004 174112          BIC  #BIT2,@PARITY IGO TO NEXT TEST
1406 005510 100001          HLT  #1
1407 005512 000010          BR   #+22
1408 005514 042777 000000 174100 C0:  BIC  #BIT2:BIT0,@PARITY IWRITE NORMAL AND DISABLE
1409 005522 000037 011550          JSR  R0,@CHECKLOC  ICHECK FOR GOOD ABORT
1410 005524 100003          HLT  #3
1411 005530 012700 001100          MOV  @STACK,SP      IABORTED INCORRECTLY
1412          IRESET THE STACK
1413 *****
1414 ITEST 31          TEST (ADDRESS) SM5,D00 CMP INSTRUCTION
1415 *****
1416 ITEST11 SCOPE
1417          11/45 *** ROM STATE 27 ***
1418 11/45 *** ROM STATE 245 ***
1419 JSR  R0,(R5)          ISET UP PARITY VECTOR SERVICE
1420 C0          IROUTINE ADDRESS
1421 005540 000010          MOV  #01,R0          ISET UP FOR A DATA
1422 005544 010010          MOV  #0,000
1423 005546 012737 005556 001332          MOV  #+10,00540DAT  IADD THE DATA
1424          ISTORE THE PC THAT SHOULD
1425          IBE PUSHED ON THE STACK
1426          IIF A PARITY ABORT OCCURS
1427          IDO A DATA
1428 005554 003000          CMP  #0(R0),R0      IWRITE NORMAL FOR EMT CALL
1429 005556 042777 000004 174036          BIC  #BIT2,@PARITY IDIDN'T ABORT
1430 005564 100001          HLT  #1
1431 005566 000010          BR   #+22
1432 005570 042777 000000 174024 C0:  BIC  #BIT2:BIT0,@PARITY IWRITE NORMAL AND DISABLE
1433 005574 000037 011550          JSR  R0,@CHECKLOC  ICHECK FOR GOOD ABORT
1434 005582 100003          HLT  #3
1435 005584 012700 001100          MOV  @STACK,SP      IABORTED INCORRECTLY
1436          IRESET THE STACK
1437 *****
1438 ITEST 32          TEST (ADDRESS) SM5,D00 CMP INSTRUCTION
1439 *****
1440 ITEST21 SCOPE
1441          11/45 *** ROM STATE 27 ***
1442 11/45 *** ROM STATE 245 ***
1443 JSR  R0,(R5)          ISET UP PARITY VECTOR SERVICE
1444 C1          IROUTINE ADDRESS
1445 005610 000010          MOV  #01,R0          ISET UP FOR A DATA
1446 005614 010010          MOV  #0,000
1447 005616 012737 005632 001332          MOV  #+12,00540DAT  IADD THE DATA
1448          ISTORE THE PC THAT SHOULD
1449          IBE PUSHED ON THE STACK
1450          IIF A PARITY ABORT OCCURS
1451          IDO A DATA
1452          IWRITE NORMAL FOR EMT CALL
1453          IDIDN'T ABORT
1454          IGO TO NEXT TEST
1455          IWRITE NORMAL AND DISABLE
1456          ICHECK FOR GOOD ABORT
1457          IABORTED INCORRECTLY
1458          IRESET THE STACK
1459 *****
1460 ITEST 33          TEST (DATA) SM5,D00 CMP INSTRUCTION
1461 *****
1462 ITEST31 SCOPE
1463          11/45 *** ROM STATE 146 ***
1464 11/45 *** ROM STATE 250 ***
1465 JSR  R0,(R5)          ISET UP PARITY VECTOR SERVICE
1466 C0          IROUTINE ADDRESS
1467 005670 005702          MOV  #01,R0          ISET UP FOR A DATA
1468 005672 011100          MOV  #012,005702  IWRITE NORMAL
1469 005674 042777 000004 173720          BIC  #BIT2,@PARITY IDO A DATA (ADDRESS NORMAL)
1470 005702 010000 177776          MOV  #012,005702  IWRITE OTHER PARITY
1471 005706 052777 000004 173700          BIC  #BIT2,@PARITY IDO A DATA (DATA OTHER PARITY)
1472 005714 011110          MOV  #01,000
1473 005716 012737 005730 001332          MOV  #+12,00540DAT  ISTORE THE PC THAT SHOULD
1474          IBE PUSHED ON THE STACK
1475          IIF A PARITY ABORT OCCURS
1476          IDO A DATA
1477 005724 042700 177776          CMP  #2(R0),R0      IWRITE NORMAL FOR EMT CALL
1478 005730 042777 000004 173664          BIC  #BIT2,@PARITY IDIDN'T ABORT
1479 005736 100001          HLT  #1
1480 005740 000010          BR   #+22
1481 005742 042777 000000 173652 C0:  BIC  #BIT2:BIT0,@PARITY IWRITE NORMAL AND DISABLE
1482 005750 000037 011550          JSR  R0,@CHECKLOC  ICHECK FOR GOOD ABORT
1483 005754 100003          HLT  #3
1484 005756 012700 001100          MOV  @STACK,SP      IABORTED INCORRECTLY
1485          IRESET THE STACK
1486 *****
1487 ITEST 34          TEST D03 JMP INSTRUCTION
1488 *****
1489 ITEST341 SCOPE
1490          11/45 *** ROM STATE 221 ***
1491 11/45 *** ROM STATE 303 ***
1492 JSR  R0,(R5)          ISET UP PARITY VECTOR SERVICE
1493 D          IROUTINE ADDRESS
1494 005766 000020          MOV  #01,R0          ISET UP FOR A DATA
1495 005770 011100          MOV  #00,000
1496 005772 012710 000006          MOV  #+10,00540DAT  IADD THE DATA
1497 005776 012737 000006 001332          MOV  #+10,00540DAT  ISTORE THE PC THAT SHOULD
1498          IBE PUSHED ON THE STACK
1499          IIF A PARITY ABORT OCCURS
1500          IDO A DATA
1501 006004 000130          JKP  #0(R0)+        IWRITE NORMAL FOR EMT CALL
1502 006006 042777 000004 173600 DD:  BIC  #BIT2,@PARITY IDIDN'T ABORT
1503 006014 100001          HLT  #1
1504 006016 000010          BR   #+22
1505 006020 000010          BIC  #BIT2:BIT0,@PARITY IWRITE NORMAL AND DISABLE
1506 006026 042777 000000 173574 D:  JSR  R0,@CHECKLOC  ICHECK FOR GOOD ABORT
1507 006037 011550          JSR  R0,@CHECKLOC
    
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1447          IIF A PARITY ABORT OCCURS
1448          IDO A DATA
1449          IWRITE NORMAL FOR EMT CALL
1450          IDIDN'T ABORT
1451          IGO TO NEXT TEST
1452          IWRITE NORMAL AND DISABLE
1453          ICHECK FOR GOOD ABORT
1454          IABORTED INCORRECTLY
1455          IRESET THE STACK
1456 *****
1457 ITEST 33          TEST (DATA) SM5,D00 CMP INSTRUCTION
1458 *****
1459 ITEST31 SCOPE
1460          11/45 *** ROM STATE 146 ***
1461 11/45 *** ROM STATE 250 ***
1462 JSR  R0,(R5)          ISET UP PARITY VECTOR SERVICE
1463 C0          IROUTINE ADDRESS
1464 005670 005702          MOV  #01,R0          ISET UP FOR A DATA
1465 005672 011100          MOV  #012,005702  IWRITE NORMAL
1466 005674 042777 000004 173720          BIC  #BIT2,@PARITY IDO A DATA (ADDRESS NORMAL)
1467 005702 010000 177776          MOV  #012,005702  IWRITE OTHER PARITY
1468 005706 052777 000004 173700          BIC  #BIT2,@PARITY IDO A DATA (DATA OTHER PARITY)
1469 005714 011110          MOV  #01,000
1470 005716 012737 005730 001332          MOV  #+12,00540DAT  ISTORE THE PC THAT SHOULD
1471          IBE PUSHED ON THE STACK
1472          IIF A PARITY ABORT OCCURS
1473          IDO A DATA
1474 005724 042700 177776          CMP  #2(R0),R0      IWRITE NORMAL FOR EMT CALL
1475 005730 042777 000004 173664          BIC  #BIT2,@PARITY IDIDN'T ABORT
1476 005736 100001          HLT  #1
1477 005740 000010          BR   #+22
1478 005742 042777 000000 173652 C0:  BIC  #BIT2:BIT0,@PARITY IWRITE NORMAL AND DISABLE
1479 005750 000037 011550          JSR  R0,@CHECKLOC  ICHECK FOR GOOD ABORT
1480 005754 100003          HLT  #3
1481 005756 012700 001100          MOV  @STACK,SP      IABORTED INCORRECTLY
1482          IRESET THE STACK
1483 *****
1484 ITEST 34          TEST D03 JMP INSTRUCTION
1485 *****
1486 ITEST341 SCOPE
1487          11/45 *** ROM STATE 221 ***
1488 11/45 *** ROM STATE 303 ***
1489 JSR  R0,(R5)          ISET UP PARITY VECTOR SERVICE
1490 D          IROUTINE ADDRESS
1491 005766 000020          MOV  #01,R0          ISET UP FOR A DATA
1492 005770 011100          MOV  #00,000
1493 005772 012710 000006          MOV  #+10,00540DAT  IADD THE DATA
1494 005776 012737 000006 001332          MOV  #+10,00540DAT  ISTORE THE PC THAT SHOULD
1495          IBE PUSHED ON THE STACK
1496          IIF A PARITY ABORT OCCURS
1497          IDO A DATA
1498 006004 000130          JKP  #0(R0)+        IWRITE NORMAL FOR EMT CALL
1499 006006 042777 000004 173600 DD:  BIC  #BIT2,@PARITY IDIDN'T ABORT
1500 006014 100001          HLT  #1
1501 006016 000010          BR   #+22
1502 006020 000010          BIC  #BIT2:BIT0,@PARITY IWRITE NORMAL AND DISABLE
1503 006026 042777 000000 173574 D:  JSR  R0,@CHECKLOC  ICHECK FOR GOOD ABORT
1504 006037 011550          JSR  R0,@CHECKLOC
    
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1501 00103F 100005 HLT +3 ABORTED INCORRECTLY
1502 000016 012706 001100 MOV #STACK,SP (RESET) THE STACK
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;THE CONTENTS OF THE STACK FOR THE NEXT TEST ARE AS FOLLOWS:
;
;1ST PUSH - ADDRESS OF THE TAG 'VDCSET' (NORMAL)
; THIS ADDRESS WOULD BE PLACED IN
; R5 UPON COMPLETION OF 'RTS R5'
; INSTRUCTION
;
;2ND PUSH
;
; . NO. OF PARAMETERS AS A FUNCTION
; . OF MEMORY INTERLEAVING
;
;NTH PUSH
;NTH +1 PUSH = MARK INSTRUCTION (OTHER PARITY)
;LAST PUSH = OLD PC FROM THE 'JSR' (NORMAL)
;
;NOTE: THE TEST SHOULD ABORT ON ATTEMPT TO FETCH THE
; MARK INSTRUCTION (NTH +1 PUSH)
;
; WHEN THE PARITY ERROR OCCURS THE STACK POINTER IS
; POSITIONED AT THE NTH +1 PUSH, THUS GIVING,
;
;NTH +2 PUSH = PS FROM PARITY ERROR
;NTH +3 PUSH = PC FROM PARITY ERROR
;

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1609
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1611
1612 006212 013406 001476 MOV #NEWSTK,SP /GET THE STACK IN PARITY
1613
1614 006216 017702 173224 MOV ENTERAD,R4 /GET THE INTERLEAVE FACTOR
1615
1616 006222 005302 DEC R2 /FOR THIS CONTROLLER
1617
1618 006224 010546 MOV R5,(SP) /CALCULATE NO. OF PARAMETERS
1619 006226 005702 TST R2 /TO BE PUSHED ON THE STACK
1620
1621 006230 001404 BEO 20 /PUSH ANY PARAMETERS TO BE
1622 006232 005302 DEC R2 /PUSHED ON THE STACK
1623 006234 012706 MOV #1,(SP) /BRANCH IF NO
1624 006240 004772 BR 13 /SUBTRACT 1 FROM PARAMETER COUNT
1625 006242 002777 006004 173358 BFI R10 #010,SPARITY /PUSH PARAMETER ONTO STACK
1626 006250 017702 173172 MOV ENTERAD,R4 /GO BACK TO SEE IF ANY MORE
1627
1628 006254 005302 DEC R2 /WRITE OTHER PARITY
1629
1630
1631 006256 002702 006004 ADD #6400,R2 /GET THE INTERLEAVE FACTOR
1632
1633 006262 010546 MOV R2,(SP) /FOR THIS CONTROLLER
1634 006264 002777 006004 173358 BIC #010,SPARITY /CALCULATE NO. OF PARAMETERS
1635 006272 010405 MOV SP,R5 /WHAT HERE TO BE PUSHED ON THE
1636
1637 006274 010402 MOV SP,R2 /STACK
1638
1639 006276 005737 001402 TST #CPU00 /CALCULATE THE CORRESPONDING
1640 006302 001002 BNE 38 /MARK INSTRUCTION
1641
1642
1643
1644 006304 002702 006004 ADD #2,R2 /PUSH MARK INSTRUCTION ON STACK
1645
1646
1647 006310 010237 001352 MOV R2,#SGDDAY /WRITE NORMAL
1648
1649
1650 006314 004767 000004 JSR PC,MRKC /PLACE MARK INSTRUCTION ADDRESS
1651 006322 104001 EI RLY #1 /INTO R5 FOR RTS
1652 006324 000407 BR #20 /GET THE PC OF THE MARK
1653 006326 000203 HMRK: RTS R5 /INSTRUCTION
1654 006328 002777 006004 173266 BFI R10 #010,SPARITY /ARE WE ON AN 11/409
1655 006334 004037 011500 JSR R0,#CHECKLOC /BRANCH IF YES
1656 006340 104003 RLY #3 /AND DON'T STEP UP THE PC
1657 006342 013706 001476 MOV #NEWSTK,SP /IT WON'T BE UPDATED ON THE
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/ THE CONTENTS OF THE STACK FOR THE NEXT TEST ARE AS FOLLOWS
/
/ 1ST PUSH = ADDRESS OF THE TAG 'VECSSET' (OTHER PARITY)
/ THIS ADDRESS WOULD BE PLACED IN
/ R5 UPON COMPLETION OF 'RTS R5'
/
/ 2ND PUSH
/
/ : NO. OF PARAMETERS AS A FUNCTION
/ : OF MEMORY INTERLEAVING
/
/ NTH PUSH
/ NTH +1 PUSH = MARK INSTRUCTION (NORMAL)
/ LAST PUSH = OLD PC FROM THE 'JSR' (NORMAL)
/
/ NOTE: THE TEST SHOULD FAIL ON ATTEMPT TO RESTORE
/ R5 CONTENTS (1ST PUSH)
/
/ WHEN THE PARITY ERROR OCCURS THE STACK POINTER IS
/ PROPERLY UPDATED, THUS GIVING,
/
/ 1ST PUSH = R5 FROM THE PARITY ERROR
/ 2ND PUSH = PC FROM THE PARITY ERROR
/
/
/ *****
/ *****
/ *****
/ *****

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1697
1698 006346 012705 011450 MOV #VECSSET,R5 /RESTORE THE PARITY VECTOR
1699
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1701
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1704 006352 000004 TST: BCOPE /SERVICE ADDRESS SETUP ROUTINE
1705
1706
1707
1708
1709 006354 004015 JSR R0,(R5) /ADDRESS
1710 006356 006510 ER /SET UP PARITY VECTOR SERVICE
1711 006360 017702 173062 MOV ENTERAD,R2 /ROUTINE ADDRESS
1712
1713
1714 006364 005302 DEC R2 /GET THE INTERLEAVE FACTOR
1715
1716 006366 002777 006004 173226 BIC #010,SPARITY /FOR THIS CONTROLLER
1717 006374 005737 001402 TST #CPU00 /CALCULATE NO. OF PARAMETERS
1718 006402 001407 BEO 38 /TO BE PUSHED ON THE STACK
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1719 006403 003700 001076      MOV  @R1,R2
1720 006406 142700 000072      SUB  #2,R2
1721
1722
1723 006412 010037 001332      MOV  R0,@SGDDAT
1724
1725
1726 006416 006403          BR   R2
1727 006420 012707 006502 001332 30:  MOV  @R1,@SGDDAT
1728
1729
1730 006426 002777 000004 173166 40:  BIS  @BIT2,@PARITY
1731 006430 010504          MOV  R5,*(SP)
1732 006436 002777 000004 173156      BIC  @BIT2,@PARITY
1733 006440 005702          R2
1734
1735 006446 001004          BEQ  Z8
1736 006450 005502          DEC  R2
1737 006452 002706 000001      MOV  @1,*(SP)
1738 006456 004772          FR   1F
1739 006460 017702 172762 20:  MOV  @INTERAD,R4
1740
1741 006464 005802          DEC  R2
1742
1743
1744 006466 002702 006400          ADD  @6400,R2
1745
1746 006472 010206          MOV  R2,*(SP)
1747 006474 010603          MOV  SP,R2
1748
1749 006476 004767 000004          JSR  PC,HRK1
1750 006502 100001          BR   #+20
1751 006504 000007          MMRK1: RTS  R5
1752 006506 000003          L0:  BIC  @BIT0,@PARITY
1753 006510 002777 000001 173104      JSR  R0,@CHECKALOC
1754 006516 004037 011530          HLT  #3
1755 006522 100003          MOV  @NEWSTK,SP
1756 006524 013706 001476          MOV  @VCCSET,R3
1757
1758
1759
1760
1761
1762
1763 006534 000004          *****
1764          TEST 41  TEST SOB BRANCH SOB INSTRUCTION
1765          *****
1766          TST41: SCOPE
1767          |
1768          |          11/05 *** ROM STATE 260 ***
1769          |
1770          |
1771          |          11/40 *** ROM STATE 1 ***
1772          |
1773          |          JSR  R0,(R5)
1774          |          F0
1775          |          BIC  @BIT2,@PARITY
1776          |          MOV  @4,R0
1777          |          *****
1778          |          WHEN DECREMENTED BY "SOB" THE
1779          |          RESULT WON'T BE 0
1780          |          ISET UP FOR A DATE
1781          |          IGET THE INITIAL TEST POINT
1782          |          IARE WE ON AN 11/40?
1783          |          IBRANCH IF NO
1784          |          ISTEP UP 1 WORD
1785          |          ISINCE THE PC WILL NOT BE
1786          |          UPDATED UN THE PARITY ABORT
1787          |          IGO TO STURE THIS VALUE
1788          |          ISTEP UP 4 WORDS
1789          |          ISINCE THE PC WILL BE UPDATED
1790          |          ION THE PARITY ABORT
1791          |          ISTORE THIS VALUE OF PC
1792          |          ITHAT SHOULD BE PUSHED ON THE
1793          |          ISTACK IF A PARITY ABORT OCCURS
1794          |          IWRITE OTHER PARITY
1795          |          IINSTRUCTION TO BE DONE ON
1796          |          ICOMPLETION OF SOB INSTRUCTION
1797          |          ITHE INSTRUCTION WILL BE IN
1798          |          IPARITY MEMORY AS DESIGNATED BY
1799          |          ITHE CONTENTS OF R1
1800          |          IMOVE AN R5 R3 INTO THE LOCATION
1801          |          IFOLLOWING THE INSTRUCTION TO BE
1802          |          IEXECUTED IN PARITY MEMORY IN
1803          |          ICASE IT DOESN'T ABORT
1804          |          IGO TO SOB INSTRUCTION
1805          |          ITHE INSTRUCTION TO BE DONE ON
1806          |          ICOMPLETION OF SOB EXECUTION IS
1807          |          IPLACED HERE
1808          |          IDIDN'T ABORT
1809          |          IGO TO RESET THE STACK
1810          |          IEXECUTE THIS INSTRUCTION
1811          |          IDISABLE PARITY
1812          |          ICHECK FOR GOOD ABORT
1813          |          IABORTED INCORRECTLY
1814          |          IRESET THE STACK
1815          |          *****
1816          |          TEST 42  TEST SOB DMO MOV INSTRUCTION
1817          |          *****
1818          |          TST42: SCOPE
1819          |          |
1820          |          |          11/40 *** ROM STATE 1 ***
1821          |          |          JSR  R0,(R5)
1822          |          |          ISET UP PARITY VECTOR SERVICE
1823          |          |          IROUTINE ADDRESS
1824          |          |          ISET UP FOR A DATE
1825          |          |          IGET THE INTERLEAVE FACTOR
1826          |          |          IFOR THIS CONTROLLER
1827          |          |          IWRITE NORMAL
1828          |          |          IANY *MOV R0,R0#S TO BE SNOVED
1829          |          |          INTO PARITY MEMORY AREA?
1830          |          |          IBRANCH IF NO

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1771
1772
1773 006554 011102          MOV  @R1,R2
1774 006556 013703 001476          MOV  @NEWSTK,R3
1775 006562 005737 001042          TST  @CPUAD
1776 006566 001403          BEQ  1S
1777 006570 002703 000002          ADD  @2,R3
1778
1779
1780 006574 000402          BR   Z8
1781 006576 002703 000004 10:  ADD  @4,R3
1782
1783
1784 006602 010337 001332 20:  MOV  R3,@SGDDAT
1785
1786
1787 006606 002777 000004 173006      BIS  @BIT2,@PARITY
1788 006614 012722 000206          MOV  @240,(R2)+
1789
1790
1791
1792
1793 006620 002777 000004 172774      BIC  @BIT2,@PARITY
1794 006626 012712 000203          MOV  @203,R2
1795
1796
1797
1798 006632 000403          BR   #+10
1799 006634 004302          F0:  JSR  R3,*(R2)
1800
1801
1802 006636 100001          HLT  #1
1803 006640 000407          BR   #+20
1804 006642 077004          SOB  R0,F
1805 006644 002777 000001 172750  F0:  BIC  @BIT0,@PARITY
1806 006652 004037 011530          JSR  R0,@CHECKALOC
1807 006656 100003          HLT  #3
1808 006660 013706 001476          MOV  @NEWSTK,SP
1809
1810
1811
1812 006664 000004          *****
1813          TEST 42  TEST SOB DMO MOV INSTRUCTION
1814          *****
1815          TST42: SCOPE
1816          |
1817          |          11/40 *** ROM STATE 1 ***
1818          |          JSR  R0,(R5)
1819          |          ISET UP PARITY VECTOR SERVICE
1820          |          IROUTINE ADDRESS
1821          |          ISET UP FOR A DATE
1822          |          IGET THE INTERLEAVE FACTOR
1823          |          IFOR THIS CONTROLLER
1824          |          IWRITE NORMAL
1825          |          IANY *MOV R0,R0#S TO BE SNOVED
1826          |          INTO PARITY MEMORY AREA?
1827          |          IBRANCH IF NO

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1843	00712	005300		DEC	R2					ISUBTRACT 1 FROM INSTRUCTION
1844										ICOUNT
1847	006710	012720	010020	MOV	#010000,(R0)+					IMOVE THE INSTRUCTION 'MOV R0,R0'
1848										ITO PARITY AREA
1849	000720	000770		BR	15					IGO BACK TO SEE IF ANY MORE!
1846	006722	002777	000000	BIT	#BIT2,0PARITY					IRWRITE OTHER PARITY
1851	006720	012720	010000	MOV	#010000,(R0)+					IMOVE THE INSTRUCTION 'MOV R0,R0'
1852										IFRINTO THE NEXT WORD LOCATION AFTER
1853										IFR THE PREVIOUS 'MOV R0,R0' INSTRUCTION
1854	006724	040777	000000	BIC	#BIT2,0PARITY					IRWRITE NORMAL
1859	006702	012710	010000	MOV	#001000,(R0)					IMOVE 'RTP R0' INTO NEXT WORD
1858										IFLOCATION AFTER THE PREVIOUS
1859	006726	045737	001000	TST	#00PU00					IF 'MOV R0,R0' INSTRUCTION IN CASE
1840	006732	001000		BEG	53					IFA PARITY ABORT DOESN'T OCCUR
1841										IFARE WE ON AN 11/40?
1842										IFRANCH IF NO
1843	006734	102700	000000	SUB	#R,R0					IFRINCL PC WILL BE UPDATED ON THE
1844										IFRITY ABORT
1845										IFRROP BACK 1 WORD ADDRESS
1846										IFRINCE THE PC WILL NOT BE
1847	006708	010037	001000	MOV	R0,00000000					IFRUPDATED ON THE PARITY ABORT
1848										IFRSTORE THE PC THAT SHOULD
1849										IFRBE PUSHED ON THE STACK
1850	006734	000771	000000	JSR	R3,R(R1)					IFRIF A PARITY ABORT OCCURS
1851	006770	100000		HLT	*1					IFRGO TO PARITY MEMORY AREA
1851	006772	000000		BR	*+10					IFRIDENTIFY ABORT
1851	006776	042777	000000	BIC	#BIT0,0PARITY					IFRGO TO RESET THE STACK
1853	007000	000037	011550	JSR	R0,00CHECKLOC					IFRDISABLE PARITY
1854	007000	100000		HLT	*3					IFRCHECK FOR GOOD ABORT
1855	007010	013700	001470	MOV	#0NEWSTK,SP					IFRABORTED INCORRECTLY
1856										IFRESET THE STACK
1857										
1858										

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```

THE CONTENTS OF THE STACK FOR THE NEXT TEST ARE AS FOLLOWS:
 1ST PUSH = OLD R3 FROM THE 'JSR' (OTHER PARITY)
 NOTE: THE TEST SHOULD FAIL ON ATTEMPT TO RESTORE
 R3 CONTENTS (1ST PUSH)

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1177
1876

1870										TEST 43 TEST RYS INSTRUCTION
1880										TST43: SCOPE
1881										11/45 *** ROM STATE 224 ***
1882	007014	000000								11/40 *** ROM STATE 325 ***
1883										JSR R0,(R5)
1884	007016	000015		GO						IFSET UP PARITY VECTOR SERVICE
1887	007020	007000		BIC	#BIT2,0PARITY					IFROUTINE ADDRESS
1888	007022	042777	000000	MOV	#0,R0					IRWRITE NORMAL
1889	007030	012720	007050	MOV	#+10,00SODAT					IFSET UP A SUBROUTINE ADDRESS
1890	007034	012737	007032	MOV						IFSTORE THE PC THAT SHOULD
1891										IFRBE PUSHED ON THE STACK
1892										IFRIF A PARITY ABORT OCCURS
1893	007042	002777	000000	BIT	#BIT2,0PARITY					IRWRITE OTHER PARITY
1894	007050	000320		JSR	R3,(R0)+					IFSUBROUTINE CALL
1895	007052	100001		HLT	*1					IFIDENTIFY ABORT
1896	007054	000012		BR	*+26					IFGO TO RESET THE STACK
1897	007056	042777	000000	BIC	#BIT2,0PARITY					IRWRITE NORMAL
1898	007064	000203		RYS	R3					IFRETURN FROM SUBROUTINE
1899	007066	042777	000001	BIC	#BIT0,0PARITY					IFDISABLE PARITY
1900	007074	000037	011550	JSR	R0,00CHECKLOC					IFRCHECK FOR GOOD ABORT
1901	007100	100000		HLT	*3					IFRABORTED INCORRECTLY
1902	007102	013700	001470	MOV	#0NEWSTK,SP					IFRESET THE STACK
1903										
1904										
1905										

```

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*****

```

THE CONTENTS OF THE STACK FOR THE NEXT TEST ARE AS FOLLOWS:
 1ST PUSH = OLD PS FOR 'RTI' (OTHER PARITY)
 2ND PUSH = OLD PC FOR 'RTI' (OTHER PARITY)
 NOTE: THE TEST SHOULD FAIL ON ATTEMPT TO 'POP'
 THE OLD PC (2ND PUSH)

WHEN THE PARITY ERROR OCCURS THE STACK POINTER
 IS POSITIONED AT THE 1ST PUSH, THUS GIVING,
 THE 'NEW' PS FOR THE RTI INSTRUCTION WHICH
 WILL OVERLAY THE OLD PC SET UP FOR THE 'RTI'
 INSTRUCTION (2ND PUSH) THUS GIVING,

1ST PUSH = OLD PS FOR 'RTI' (OTHER PARITY)
 2ND PUSH = NEW PS FROM THE 'RTI'
 3RD PUSH = NEW PC FROM THE 'RTI'

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*****

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1935  
1936  
1937  
1938  
1939  
1940 007106 000004  
1941  
1942  
1943  
1944 007110 000013  
1945 007112 000100  
1946 007114 012726 000340  
1947 007120 012746 007134  
1948 007124 012737 007134 001332  
1949  
1950  
1951 007132 000000  
1952 007134 104001  
1953 007134 012747  
1954 007140 002777 000000 172400  
1955 007144 012747  
1956 007150 002777 000001 172400  
1957 007154 002737 011550  
1958 007162 100003  
1959 007166 012766 001476  
1960  
1961  
1962  
1963  
1964  
1965  
1966  
1967  
1968  
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2037  
2038  
2039  
2040
```

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*****  
*****  
*****  
/ THE CONTENTS OF THE STACK FOR THE NEXT TEST ARE AS FOLLOWS:  
/ 1ST PUSH = OLD PS FOR 'RTI' (OTHER PARITY)  
/ 2ND PUSH = OLD PC FOR 'RTI' (OTHER PARITY)  
/ NOTE: THE TEST SHOULD FAIL ON ATTEMPT TO 'POP'  
/ THE OLD PS (1ST PUSH)  
/ THE 2ND PUSH IS REWRITTEN NORMAL BEFORE  
/ DURING THE 'RTI'  
/ WHEN THE PARITY ERROR OCCURS THE STACK  
/ POINTER IS PROPERLY UPDATED AND THE NEW  
/ PS AND PC FROM THE RTI INSTRUCTION IS  
/ PUSHED ONTO THE STACK, THUS GIVING,  
/ 1ST PUSH = NEW PS FROM 'RTI' (NORMAL)  
/ 2ND PUSH = NEW PC FROM 'RTI' (NORMAL)  
*****  
*****  
*****
```

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1993  
1994  
1995  
1996  
1997  
1998 007170 000000  
1999  
2000  
2001  
2002  
2003  
2004  
2005  
2006  
2007  
2008  
2009 007214 000402  
2010 007216 104001  
2011 007220 000416  
2012 007222 002777 000005 172372  
2013 007230 012646  
2014  
2015 007232 002777 000001 172362  
2016 007240 000002  
2017 007242 002777 000001 172352  
2018 007250 004037 011550  
2019 007254 104003  
2020 007256 012766 001166  
2021  
2022  
2023  
2024  
2025  
2026  
2027 007262 000004  
2028  
2029  
2030  
2031 007264 004015  
2032 007266 007320  
2033 007270 011160  
2034 007272 010020  
2035 007274 012737 007306 001332  
2036  
2037  
2038 007302 006056 177776  
2039 007306 002777 000004 172306  
2040 007314 104001
```

```
*****  
*****  
*****  
/ TEST 45 TEST 2ND POP RTI INSTRUCTION  
/ *****  
/ TST45: SCOPE 11/45 *** ROM STATE 214 ***  
/ /  
/ 11/40 *** ROM STATE 322 ***  
/ JSR R0,(R5) /SET UP PARITY VECTOR SERVICE  
/ H2 /ROUTINE ADDRESS  
/ MOV #340,(SP) /SET PS FOR 'RTI'  
/ MOV #R3,(SP) /SET RETURN FROM 'RTI'  
/ MOV #R3,#000DAT /STORE THE PC THAT SHOULD  
/ /BE PUSHED ON THE STACK  
/ /IF A PARITY ABORT OCCURS  
/ /GO DO 'RTI'  
/ /DON'T ABORT  
/ /GO TO RESET THE STACK  
/ /WRITE NORMAL AND DISABLE PARITY  
/ /WRITE 181 'POP' NORMAL SO NO  
/ /ERROR WILL OCCUR  
/ /ENABLE PARITY  
/ /RETURN FROM INTERRUPT  
/ /DISABLE PARITY  
/ /CHECK FOR GOOD ABORT  
/ /ABORTED INCORRECTLY  
/ /INITIALIZE THE STACK BACK  
/ /TO 1100 FOR WE DON'T NEED IT  
/ /TO BE IN PARITY MEMORY AREA  
/ /ANYMORE  
/ *****  
/ TEST 46 TEST (DATA) SM8,DH0 ADD INSTRUCTION  
/ *****  
/ TST46: SCOPE 11/45 *** ROM STATE 142 ***  
/ /  
/ 11/40 *** ROM STATE 250 ***  
/ JSR R0,(R5) /SET UP PARITY VECTOR SERVICE  
/ L /ROUTINE ADDRESS  
/ MOV #R1,R0 /SET UP FOR A DATA  
/ MOV R0,(R0)+ /DO THE DATA  
/ MOV #012,#000DAT /STORE THE PC THAT SHOULD  
/ /BE PUSHED ON THE STACK  
/ /IF A PARITY ABORT OCCURS  
/ /GO A DATA  
/ /WRITE NORMAL FOR EMT CALL  
/ /DON'T ABORT
```

```
2041 007316 004010          BR      +20          IGO TO NEXT TEST
2042 007318 004012 000005 172274 LI      BIC      @BIT2,@PARITY  IWRITE NORMAL AND DISABLE
2043 007320 004037 011500          JSR      R0,@CHECKLOC  ICHECK FOR GOOD ABORT
2044 007322 004003          HLT      +3          IABORTED INCORRECTLY
2045 007324 012706 001100          MOV      @STACK,SP     IRESET THE STACK
*****
IEST 47      TEST DMI 1310 INSTRUCTION
*****
TST47: SCOPE
I
I      11/45 *** ROM STATE 175 ***
I
I      11/40 *** ROM STATE 207 ***
2053 007302 004015          JSR      R0,(R5)      ISET UP PARITY VECTOR SERVICE
2054 007304 007376          M          IROUTINE ADDRESS
2055 007306 011100          MOV      @R1,R0      ISET UP FOR A DATO
2056 007308 010010          MOV      R0,(R0)      IDO THE DATO
2057 007352 012737 007362 001332 MOV      @,+10,@@SODAT ISTORE THE PC THAT SHOULD
2058          IBE PUSHED ON THE STACK
2059          IIF A PARITY ABORT OCCURS
2060 007364 100010          TSTB    (R0)          IDO A DATA
2061 007366 004010          BIC      @BIT2,@PARITY IWRITE NORMAL FOR EMT CALL
2062 007370 104001          HLT      +1          IDIDN'T ABORT
2063 007372 004010          BR      +22          IGO TO NEXT TEST
2064 007374 004010          BIC      @BIT2,@PARITY IWRITE NORMAL AND DISABLE
2065 007376 004037 011500          JSR      R0,@CHECKLOC  ICHECK FOR GOOD ABORT
2066 007378 104003          HLT      +3          IABORTED INCORRECTLY
2067 007410 012706 001100          MOV      @STACK,SP     IRESET THE STACK
*****
IEST 50      TEST DMS CLR8 INSTRUCTION
*****
TBY50: SCOPE
I
I      11/45 *** ROM STATE 221 ***
I
I      11/40 *** ROM STATE 264 ***
2075 007416 004015          JSR      R0,(R5)      ISET UP PARITY VECTOR SERVICE
2076 007420 007450          NH          IROUTINE ADDRESS
2077 007422 011100          MOV      @R1,R0      ISET UP FOR A DATO
2078 007424 010010          MOV      R0,(R0)      IDO THE DATO
2079 007426 012737 007436 001332 MOV      @,+10,@@SODAT ISTORE THE PC THAT SHOULD
2080          IBE PUSHED ON THE STACK
2081          IIF A PARITY ABORT OCCURS
2082 007434 100010          CLR8    @ (R0)+       IDO A DATA
2083 007436 004010          BIC      @BIT2,@PARITY IWRITE NORMAL FOR EMT CALL
2084 007440 104001          HLT      +1          IDIDN'T ABORT
2085 007442 004010          BR      +22          IGO TO NEXT TEST
2086 007444 004010          BIC      @BIT2,@PARITY IWRITE NORMAL AND DISABLE
2087 007446 004037 011500          JSR      R0,@CHECKLOC  ICHECK FOR GOOD ABORT
2088 007448 104003          HLT      +3          IABORTED INCORRECTLY
2089 007464 012706 001100          MOV      @STACK,SP     IRESET THE STACK
*****
IEST 51      TEST SMI SUBTRACT INSTRUCTION
*****
TST51: SCOPE
I
I      11/45 *** ROM STATE 27 ***
```

```
2095          I
2096          I      11/40 *** ROM STATE 250 ***
2097 007472 004015          JSR      R0,(R5)      ISET UP PARITY VECTOR SERVICE
2098 007474 007526          P          IROUTINE ADDRESS
2099 007476 011100          MOV      @R1,R0      ISET UP FOR A DATO
2100 007500 010010          MOV      R0,(R0)      IDO THE DATO
2101 007502 012737 007512 001332 MOV      @,+10,@@SODAT ISTORE THE PC THAT SHOULD
2102          IBE PUSHED ON THE STACK
2103          IIF A PARITY ABORT OCCURS
2104 007510 101027 177777          SUB      (R0),@177777 IDO A DATA
2105 007514 004010          BIC      @BIT2,@PARITY IWRITE NORMAL FOR EMT CALL
2106 007520 104001          HLT      +1          IDIDN'T ABORT
2107 007522 004010          BR      +22          IGO TO NEXT TEST
2108 007524 004010          BIC      @BIT2,@PARITY IWRITE NORMAL AND DISABLE
2109 007526 004037 011500          JSR      R0,@CHECKLOC  ICHECK FOR GOOD ABORT
2110 007528 104003          HLT      +3          IABORTED INCORRECTLY
2111 007542 012706 001100          MOV      @STACK,SP     IRESET THE STACK
*****
IEST 52      TEST (DATA) SMS B158 INSTRUCTION
*****
T152: SCOPE
I
I      11/45 *** ROM STATE 177 ***
I
I      11/40 *** ROM STATE 245 ***
2119 007550 004015          JSR      R0,(R5)      ISET UP PARITY VECTOR SERVICE
2120 007552 007604          R          IROUTINE ADDRESS
2121 007554 011100          MOV      @R1,R0      ISET UP FOR A DATO
2122 007556 010010          MOV      R0,(R0)      IDO THE DATO
2123 007560 012737 007570 001332 MOV      @,+10,@@SODAT ISTORE THE PC THAT SHOULD
2124          IBE PUSHED ON THE STACK
2125          IIF A PARITY ABORT OCCURS
2126 007566 100000 000002          B100    @-(R0),2(R0)  IDO A DATA
2127 007572 004010          BIC      @BIT2,@PARITY IWRITE NORMAL FOR EMT CALL
2128 007600 104001          HLT      +1          IDIDN'T ABORT
2129 007602 004010          BR      +22          IGO TO NEXT TEST
2130 007604 004010          BIC      @BIT2,@PARITY IWRITE NORMAL AND DISABLE
2131 007606 004037 011500          JSR      R0,@CHECKLOC  ICHECK FOR GOOD ABORT
2132 007610 104003          HLT      +3          IABORTED INCORRECTLY
2133 007620 012706 001100          MOV      @STACK,SP     IRESET THE STACK
*****
IEST 53      NEXT 4 TESTS ARE NON-INTERLEAVE AS DEPENDENT
*****
T153: SCOPE
I
I      2137 007624 000004          CMP      @17360,R1     IHAVE WE PARITY IN THE LOWER 4K?
2138 007626 004010          IAND    @17360,R1     IAND IS THE SELECTED REGISTER
2139          IGOVERNING THE 4K AREA?
2140          ITHIS COMPARE IS IF THE RT11
2141          IISNOT ENABLED DURING PROGRAM
2142          IEXECUTION
2143          IYES - PROCEED TO NEXT TESTS
2144 007632 101005          BMI      @5          IWHICH DEPEND ON PARITY IN LOWER 4K
2145          ITHE ABOVE COMPARE DIDN'T
2146 007634 004010          CMP      @140,@@SCLAD ICHECK - DO THIS COMPARE TO
2147          ISEE IF I WAS BECAUSE THE
```

2188										
2189										
2191	007640	011001			BEO	25				ITABLE WAS CREATED WITH MEMORY
2192										MANAGEMENT TURNED ON
2193	007644	000004			BR	45				PROCEED TO NEXT TESTS IF THIS
2194										ISCOMPARE CHECKS
2195										ITHE REGISTER UNDER TEST IS NOT
2196										CONTROLLED BY THE LOWER BR!!!
2197										GO TO RETVALDRM BISTNM AND
2198	007646	042177	000001	171072	Z-1	CHP	01,ENTERAD			7 JUMP OVER THE (R) SK DEPENDENT
2199										TESTS
2199	007654	001005				BEO	35			IF THIS CONTROLLER
2199	007656	002107	000004	171010	R-1	ADD	04,SYSTNM			INTERLEAVED?
2199										IFRANCH IF NO
2199										IFSET SYSTEM TO PROPER VALUE
2199										IF SINCE THE NEXT 4 TESTS WILL
2199										BE SKIPPED
2199	007664	000137	010426			JMP	00RED005			IF JUMP TO 187 INDEX WORD TEST
2199	007670	000037	001024			INC	00PCCORZONES			IF SET FLAG INDICATING TO CHECKLOC
2199										IF ROUTINE THAT PS AND PC FETCH
2199										IF AND RED & YELLOW ZONE AREAS
2199										IF ARE GOING TO BE TESTED

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THE CONTENTS OF THE STACK FOR THE NEXT TEST ARE AS FOLLOWS:

1ST PUSH = OLD PS FROM ERROR TRAP (NORMAL)
 2ND PUSH = OLD PC FROM ERROR TRAP (NORMAL)

NOTE: THE TEST SHOULD FAIL ON ATTEMPT TO FETCH THE NEW PS
 WHEN THE PARITY ERROR OCCURS THE STACK POINTER IS
 ALTERED FROM THE ORIGINAL ERROR TRAP, THUS GIVING,

1ST PUSH = OLD PS FROM ERROR TRAP (NORMAL)
 2ND PUSH = NEW PS FROM THE PARITY ERROR
 3RD PUSH = NEW PC FROM THE PARITY ERROR

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2197										
2198										
2199										*****
2200										TEST 54 TEST NEW 'PS' FETCH
2201										*****
2202	007674	000004								TEST4: SCBPE

2203										
2204										
2205										11/45 *** ROM STATE 357 ***
2206	007676	004019			JSR	R0,(R5)				IF SET UP PARITY VECTOR SERVICE
2207	007700	007754			TB					IF ROUTINE ADDRESS
2208	007702	042177	000004	171112	BIC	#BIT2,@PAKITY				IF WRITE NORMAL
2209	007710	012737	007750	000010	MOV	#T,@RESVLC				IF RESERVED INSTRUCTION TIMEOUT
2210										IF VECTOR ADDRESS
2211	007716	012737	007750	001332	MOV	#1,@SGDDAT				IF STORE THE PC THAT SHOULD
2212										IF BE PUSHED ON THE STACK
2213										IF IF A PARITY AORT OCCURS
2214	007724	052777	000004	171670	BIS	#BIT2,@PAKITY				IF WRITE OTHER PARITY
2215	007732	012737	000300	000012	MOV	#340,@RESVLC+2				IF NEW 'PS' FOR ERROR TRAP
2216	007740	042777	000004	171654	BIC	#BIT2,@PAKITY				IF WRITE NORMAL
2217	007746	007660			Y000					IF (NON-RECOGNIZABLE OP-CODE
2218										IF SHOULD ATTEMPT TO TRAP TO 'Y:'
2219	007750	104001			Y!	HLT	+1			IF DIDN'T AORT
2220	007752	000000			BR	.*16				IF GO TO RESET THE STACK
2221	007754	042777	000001	171640	TW!	BIC	#BIT0,@PAKITY			IF DISABLE PARITY
2222	007762	000037	011550		JSR	R0,@CHECKLOC				IF CHECK FOR GOOD AORT
2223	007766	104003			HLT	+3				IF AORTED INCORRECTLY
2224	007770	012706	001100		MOV	@STACK,SP				IF RESET THE STACK
2225										
2226										
2227										

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*****
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*****

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THE CONTENTS OF THE STACK FOR THE NEXT TEST ARE AS FOLLOWS:

1ST PUSH = OLD PS FROM ERROR TRAP (NORMAL)
 2ND PUSH = OLD PC FROM ERROR TRAP (NORMAL)

NOTE: THE TEST SHOULD FAIL ON ATTEMPT TO FETCH THE NEW PC
 WHEN THE PARITY ERROR OCCURS THE STACK POINTER IS
 NOT ALTERED FROM THE ORIGINAL ERROR TRAP AND THE
 PC FOR THE PARITY ERROR IS THE OLD PS, THUS GIVING,

1ST PUSH = OLD PS FROM ERROR TRAP (NORMAL)
 2ND PUSH = OLD PS FROM ERROR TRAP (NORMAL)

IN OTHER WORDS THE ORIGINAL ERROR TRAP AND VECTOR IS LOS!!!!

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```
2365 010166 042777 000001 I1ERRM V41 BIC @BIT0,@PARITY 0DISABLE PARITY
2366 010170 042787 000372 TST @R372 ISTACK VIOLATION PICKED UP -
2367 I WAS THE PARITY ABORT?
2368 010200 041982 LNE 10 IBRANCH TO YES
2369 010200 040019 HLT +12 ISTACK VIOLATION PICKED UP
2370 I BUT ABORT NOT RECOGNIZED
2371 010200 040007 BR YELLOW IGO TO RESET STACK AND RESTORE
2372 I TIMEOUT VECTORS
2373 010200 010100 001100 I1I MOV @STACK,SP IPRESET STACK BACK TO NORMAL
2374 010210 010170 000372 MOV @R372,@(SP) IPUSH ABORT PC ONTO NORMAL
2375 ISTACK OUT OF VIOLATION AREA
2376 I FOR CHECKING PURPOSES
2377 010210 040037 011500 JNR R0,@CHECKN,LOC I CHECK FOR GOOD ABORT
2378 010212 100000 HLT +3 I ABORTED INCORRECTLY
2379 010220 010100 001100 YELLOW MOV ISTACK,SP IPRESET STACK BACK TO NORMAL
2380 010230 010170 010170 000004 MOV @@AVLOC,@R4 IPRESERVE CONTENTS OF LOC. 4
2381 010230 005037 000006 CLR @R6 IPRESTORE CONTENTS OF LOC. 4
2382 010240 040037 000372 CLR @R372 IPRESET TRAPCATCHER LOCATION
2383 I FOR NEXT TEST
2384 010240 045037 000340 CLR @R340 ICLEAR BOTTOM LIMIT LOCATION
2385 I OF 'YELLOW' ZONE USING NORMAL
2386 I PARITY
2387
2388
2389
```

/
/ THE FOLLOWING TEST WILL/S COULD CAUSE A PARITY ABORT IN THE
/ 'RED' ZONE. THE 'RED' ZONE IS THE AREA BEYOND THE 'YELLOW'
/ ZONE DESCRIBED IN THE ABOVE TEST. I.E. LOCATIONS 336 ON
/ DOWN COMPRISE THE 'RED' ZONE
/
/ SINCE PARITY ERRORS HAVE HIGHER PRIORITY WE WILL BE LOOKING
/ FOR THE PARITY ABORT TO OCCUR BEFORE THE STACK VIOLATION
/ TRAP TO LOCATION 4.
/
/ THE CONTENTS OF THE STACK AFTER EXECUTION OF THE NEXT TEST
/ SHOULD BE AS FOLLOWS:
/
/ LOC. 0 = PC FROM STACK VIOLATION
/ LOC. 2 = PS FROM STACK VIOLATION
/
/ NOTE: THE PS AND PC FROM THE STACK VIOLATION ARE IN LOC. 0 & 2
/ BECAUSE THE STACK POINTER (R6) IS REPOSITIONED TO LOC. 4
/ (BY HARDWARE)!!
/
/
/

```
LOC. 374 = PS FROM PARITY ABORT
LOC. 372 = PC FROM PARITY ABORT
/
/ NOTE: THE ABOVE CONTENTS WILL EXIST IN THE 2 DIFFERENT STACK
/ AREAS IF BOTH THE PARITY ERROR AND STACK VIOLATION WERE
/ RECOGNIZED. THE TEST BELOW WILL DIFFERENTIATE BETWEEN
/ WHICH OCCURRED FIRST.
/
/
/ *****
/ *****
/ *****
/ *****
2409
2410
2411
2412
2413
2414
2415 010252 000004 TEST 57 TEST ABORT IN 'RED' ZONE
2416 I *****
2417 I *****
2418 I *****
2419 I *****
2420
2421
2422 010254 000015 JSR R0,(R5) ISET UP A PARITY VECTOR SERVICE
2423 010256 010336 V3 IROUTINE ADDRESS
2424 010260 005037 000336 CLR @R336 ICLEAR 1ST LOCATION OF 'RED'
2425 I ZONE USING 'OTHER' PARITY
2426 010264 042777 000004 171330 BIC @BIT2,@PARITY IWRITE NORMAL
2427 010272 012700 000376 MOV @370,SP ISET STACK IN 'YELLOW' AREA
2428 010276 013727 000000 MOV @R4,(PC) ISAVE CONTENTS OF LOC. 4
2429 I IN NEXT LOCATION
2430 010300 000000 HULLDLOC4: .WORD 0 IORIGINAL CONTENTS OF LOC. 4
2431 I GO HERE
2432 010304 012737 010342 000004 MOV @V4,@ERRVEC ISET UP A TIMEOUT VECTOR SERVICE
2433 I ROUTINE ADDRESS FOR STACK
2434 I VIOLATION
2435 010310 012737 000340 000006 MOV @340,@ERRVEC+2 INEW PS ON TIMEOUT TRAP
2436 010320 012737 010332 001332 MOV @,+12,@R5,00AT ISTORE THE PC THAT SHOULD
2437 I BE PUSHED ON THE STACK
2438 010326 005766 177740 TST =40(SP) IIF A PARITY ABORT OCCURS
2439 I REFERENCE 1ST LOCATION OF 'RED'
2440 I ZONE USING REGISTER 6
2441 I THIS INSTRUCTION SHOULD CAUSE
2442 I AN ABORT 1ST, THEN A STACK
2443 I VIOLATION
2444 010332 104010 HLT +10 I DIDN'T ABORT OR RECOGNIZE THE
2445 I STACK VIOLATION
2446 010334 000421 BR RED IGO TO RESET STACK AND RESTORE
2447 I TIMEOUT VECTORS
2448 010336 104011 V3: HLT +11 I ABORTED BUT STACK VIOLATION
2449 I NOT RECOGNIZED
2450 010340 000417 BR RED IGO TO RESET STACK AND RESTORE
2451 I TIMEOUT VECTORS
2452 010342 042777 000001 171252 V41 BIC @BIT0,@PARITY IDISABLE PARITY
2453 010350 005737 000372 TST @R372 ISTACK VIOLATION PICKED UP -
```

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2470
2471 010054 001870 ENR 18
2472 010056 104610 HLT *18
2473
2474 010058 000037 OR RED
2475
2476 010060 012750 001100 IPI MOV *STACK,SP
2477 010062 013740 000037 NOV *0370,-(SP)
2478
2479
2480 010072 004037 011050 JSR R0,*CHECKLOC
2481 010074 100003 HLT *3
2482 010076 012700 001100 RFD1 MOV *STACK,SP
2483 010078 013737 010030 000000 *HOLDLOC,*R0
2484 010080 015007 000036 *6
2485 010082 000037 000037 CLR *0370
2486
2487 010088 000037 000036 CLR *0336
2488
2489 *****
2490 FIRST 62 TEST (INDEX WORD) SHD,DMS MOV INSTRUCTION
2491 *****
2492 TST01: SCOPE
2493 I
2494 I
2495 I 11/40 **** ROM STATE 200 ****
2496 010090 000037 010084 CLR *0000020000 /CLEAR PS, PC DR ZONES
2497
2498 JSR R0,(R5) /SET UP PARITY VECTOR SERVICE
2499 010092 010014 W /ROUTINE ADDRESS
2500 010094 002777 000004 171154 BIC *BIT2,*PARITY /WRITE NORMAL
2501 010096 010100 MOV R1,R0 /SET UP FOR A DATO
2502 010098 012720 010070 MOV *10070,(R0)+ /MOVE THE INSTRUCTION
2503 / *MOV R0,*2(R0)* TO PARITY
2504 MEMORY AREA
2505 010099 052777 000004 171140 BIS *BIT2,*PARITY /WRITE OTHER PARITY
2506 010102 012720 177776 MOV *+2,(R0)+ /WRITE THE INDEX WORD IN NEXT
2507 /PARITY MEMORY AREA LOCATION
2508 010104 002777 000004 171126 BIC *BIT2,*PARITY /WRITE NORMAL
2509 010106 010037 001332 MOV R0,*000DAT /STORE THE PC THAT SHOULD
2510 /BE PUSHED ON THE STACK
2511 /IF A PARITY ABORT OCCURS
2512 010108 012710 000203 MOV *203,(R0) /MOVE *RTD R0* INTO NEXT
2513 /PARITY MEMORY AREA LOCATION
2514 /IN CASE HE DON'T ABORT
2515 /GO TO PARITY MEMORY AREA
2516 JSR R3,*4(R0) /DIDN'T ABORT
2517 010110 104001 HLT *1
2518 010112 000010 BR *+22 /GO TO NEXT TEST
2519 010114 002777 000001 171100 BIC *BIT0,*PARITY /DISABLE PARITY
2520 010116 004037 011050 JSR R0,*CHECKLOC /CHECK FOR GOOD ABORT
2521 010118 104003 HLT *3 /ABORTED INCORRECTLY
2522 010120 012700 001100 MOV *STACK,SP /RESET THE STACK
2523 *****
2524 ITEST 61 TEST (INDEX WORD) SHD,DMS CNP INSTRUCTION

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2524 *****
2525 010530 000004 TST01: SCOPE
2526 I
2527 I
2528 I 11/40 **** ROM STATE 241 ****
2529 010534 004015 JSR R0,(R5) /SET UP PARITY VECTOR SERVICE
2530 010536 010014 XX /ROUTINE ADDRESS
2531 010538 002777 000004 171052 BIC *BIT2,*PARITY /WRITE NORMAL
2532 010540 010100 MOV R1,R0 /SET UP FOR A DATO
2533 010542 012720 000000 MOV *20000,(R0)+ /MOVE THE INSTRUCTION
2534 / *MOV *2(R0),R0* TO PARITY
2535 MEMORY AREA
2536 010544 052777 000004 171036 BIS *BIT2,*PARITY /WRITE OTHER PARITY
2537 010546 012720 177776 MOV *+2,(R0)+ /WRITE THE INDEX WORD IN NEXT
2538 /PARITY MEMORY AREA LOCATION
2539 010548 002777 000004 171024 BIC *BIT2,*PARITY /WRITE NORMAL
2540 010550 010037 001332 MOV R0,*000DAT /STORE THE PC THAT SHOULD
2541 /BE PUSHED ON THE STACK
2542 /IF A PARITY ABORT OCCURS
2543 010552 012710 000203 MOV *203,(R0) /MOVE *RTD R0* INTO NEXT
2544 /PARITY MEMORY AREA LOCATION
2545 /IN CASE HE DON'T ABORT
2546 010554 004360 177776 JSR R3,*4(R0) /GO TO PARITY MEMORY AREA
2547 010556 104001 HLT *1 /DIDN'T ABORT
2548 010558 000014 BR *+22 /GO TO NEXT TEST
2549 010560 002777 000001 170776 XXI BIC *BIT0,*PARITY /DISABLE PARITY
2550 010562 004037 011050 JSR R0,*CHECKLOC /CHECK FOR GOOD ABORT
2551 010564 104003 HLT *3 /ABORTED INCORRECTLY
2552 010566 012700 001100 MOV *STACK,SP /RESET THE STACK
2553 *****
2554 ITEST 62 TEST (INDEX WORD) SHD,DMS MOV INSTRUCTION
2555 *****
2556 010636 000004 TST02: SCOPE
2557 I
2558 I
2559 I 11/40 **** ROM STATE 206 ****
2560 010640 004015 JSR R0,(R5) /SET UP PARITY VECTOR SERVICE
2561 010642 010720 Y /ROUTINE ADDRESS
2562 010644 002777 000004 170750 BIC *BIT2,*PARITY /WRITE NORMAL
2563 010646 010100 MOV R1,R0 /SET UP FOR A DATO
2564 010648 012720 010000 MOV *10000,(R0)+ /MOVE THE INSTRUCTION
2565 / *MOV R0,*2(R0)* TO PARITY
2566 MEMORY AREA
2567 010650 052777 000004 170734 BIS *BIT2,*PARITY /WRITE OTHER PARITY
2568 010652 012720 177776 MOV *+2,(R0)+ /WRITE THE INDEX WORD IN NEXT
2569 /PARITY MEMORY AREA LOCATION
2570 010654 002777 000004 170722 BIC *BIT2,*PARITY /WRITE NORMAL
2571 010656 010037 001332 MOV R0,*000DAT /STORE THE PC THAT SHOULD
2572 /BE PUSHED ON THE STACK
2573 /IF A PARITY ABORT OCCURS
2574 010658 012710 000203 MOV *203,(R0) /MOVE *RTD R0* INTO NEXT
2575 /PARITY MEMORY AREA LOCATION
2576 /IN CASE HE DON'T ABORT
2577 010660 004360 177776 JSR R3,*4(R0) /GO TO PARITY MEMORY AREA

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2576 010711 104001      MLY      *1      /DIDN'T ABORT
2579 010714 000410      BR      *+22     /GO TO NEXT TEST
2580 010720 002777 000001 170574 Y:  BIC     @BIT0,@PARITY /DISABLE PARITY
2581 010720 002777 011550  JSR     R0,@CHECKLOC /CHECK FOR GOOD ABORT
2582 010720 104003      MLY      *3      /ABORTED INCORRECTLY
2583 010720 012700 001100  MOV     @STACK,SP /RESET THE STACK
2584      /*****
2585 ITEST 65      TEST   BPL INSTRUCTION (=BOK)
2586      /*****
2587 010740 000004      YST64: SCOPE
2588      /
2589      /
2590      /
2591 010740 004015      I      11/45 *** ROM STATE 321 ***
2592 010740 011050      JSR     R0,(R5)      /SET UP PARITY VECTOR SERVICE
2593 010740 002777 000000 170066  Z0      /ROUTINE ADDRESS
2594 010750 012102      BIC     @BIT2,@PARITY /WRITE NORMAL
2595 010750 102700 000004  MOV     R1,R2      /SET UP FOR A DATO
2596      SUB     @4,R2      /CALCULATE THE START
2597 010760 012722 010700  MOV     @12700,(R0)+ /MOVE THE INSTRUCTION
2598      /MOV @0,R0 INTO PARITY
2599 010760 012722 177777  MOV     @-1,(R2)+   /MOVE A 0 INTO NEXT
2600      /PARITY MEMORY AREA LOCATION
2601 010770 012722 100001  MOV     @10001,(R2)+ /MOVE THE INSTRUCTION
2602      /ONE ,+0 INTO NEXT
2603 010770 002777 000004 170516  BIS     @BIT0,@PARITY /PARITY MEMORY AREA LOCATION
2604 011004 005737 001042  TST     @CPU40      /WRITE OTHER PARITY
2605 011010 001405      BEQ     Z0          /ARE WE ON AN 11/40?
2606      /BRANCH IF NO
2607      /SINCE THE PC WILL BE UPDATED
2608      /FOR THE PARITY ABORT
2609 011010 010237 001332  MOV     R2,@SGDDAT /STORE THIS PC THAT SHOULD
2610      /BE PUSHED ON THE STACK
2611      /IF A PARITY ABORT OCCURS
2612      /SINCE THE PC IS NOT UPDATED
2613      /FOR THE PARITY ABORT
2614      /MOVE A 'TOP' INTO NEXT
2615 011010 010722 000240  MOV     @240,(R2)+  /PARITY MEMORY AREA LOCATION
2616      /CONTINUE WITH TEST
2617 011020 000004      BR      10          /MOVE A 'TOP' INTO NEXT
2618 011024 012722 000240 20:  MOV     @240,(R2)+  /PARITY MEMORY AREA LOCATION
2619      /STORE THE PC THAT SHOULD
2620      /BE PUSHED ON THE STACK
2621      /IF A PARITY ABORT OCCURS
2622      /WRITE NORMAL
2623 011030 002777 000004 170560 10:  BIS     @BIT2,@PARITY /CHECK FOR GOOD ABORT
2624 011040 012712 000203  MOV     @203,(R2)  /CHECK FOR GOOD ABORT
2625      /MOVE 'R2> R3' INTO NEXT
2626      /PARITY MEMORY AREA LOCATION
2627 011040 004362 177770  JSR     R3,-10(R2) /IN CASE WE DON'T ABORT
2628 011050 104003      MLY      *1      /GO TO PARITY MEMORY AREA
2629 011050 000410      BR      *+22     /DIDN'T ABORT
2630 011050 002777 000001 170536 Z0:  BIC     @BIT0,@PARITY /DISABLE PARITY
2631 011060 004037 011550  JSR     R0,@CHECKLOC /CHECK FOR GOOD ABORT
    
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2632 011070 104003      MLY      *3      /ABORTED INCORRECTLY
2633 011070 012700 001100  MOV     @STACK,SP /RESET THE STACK
2634      /*****
2635 ITEST 64      TEST   BPL INSTRUCTION (=BOK)
2636      /*****
2637 011070 000004      YST64: SCOPE
2638      /
2639      /
2640      /
2641 011100 004015      I      11/40 *** ROM STATE 1 ***
2642 011100 011214      JSR     R0,(R5)      /SET UP PARITY VECTOR SERVICE
2643 011100 002777 000004 170510  Z1      /ROUTINE ADDRESS
2644 011110 010102      BIC     @BIT2,@PARITY /WRITE NORMAL
2645 011114 102700 000004  MOV     R1,R2      /SET UP FOR A DATO
2646      SUB     @4,R2      /CALCULATE THE START ADDRESS
2647 011120 012722 012700  MOV     @12700,(R0)+ /MOVE THIS TEST
2648      /MOVE THE INSTRUCTION
2649      /MOV @0,R0 INTO PARITY
2650      /MEMORY AREA
2651 011120 012722 000000  MOV     @0,(R2)+   /MOVE A 0 INTO NEXT PARITY
2652 011130 012722 001001  MOV     @1001,(R2)+ /MEMORY AREA LOCATION
2653      /MOVE THE INSTRUCTION
2654      /ONE ,+0 INTO NEXT
2655 011130 002777 000004 170460  BIS     @BIT2,@PARITY /PARITY MEMORY AREA LOCATION
2656 011140 005737 001042  TST     @CPU40      /WRITE OTHER PARITY
2657 011140 001405      BEQ     Z0          /ARE WE ON AN 11/40?
2658      /BRANCH IF NO
2659      /SINCE THE PC WILL BE UPDATED
2660      /FOR THE PARITY ABORT
2661 011150 010237 001332  MOV     R2,@SGDDAT /STORE THIS PC THAT SHOULD
2662      /BE PUSHED ON THE STACK
2663      /IF A PARITY ABORT OCCURS
2664      /SINCE THE PC WILL NOT BE
2665      /UPDATED ON THE PARITY ABORT
2666 011150 012722 000240  MOV     @240,(R2)+  /MOVE A 'TOP' INTO NEXT
2667 011160 000004      BR      10          /PARITY MEMORY AREA LOCATION
2668 011160 012722 000240 20:  MOV     @240,(R2)+  /CONTINUE WITH TEST
2669      /MOVE A 'TOP' INTO NEXT
2670 011160 010237 001332  MOV     R2,@SGDDAT /PARITY MEMORY AREA LOCATION
2671      /STORE THE PC THAT SHOULD
2672      /BE PUSHED ON THE STACK
2673 011170 002777 000004 170422 10:  BIS     @BIT2,@PARITY /CHECK FOR GOOD ABORT
2674 011200 012712 000203  MOV     @203,(R2)  /CHECK FOR GOOD ABORT
2675      /MOVE 'R2> R3' INTO NEXT
2676      /PARITY MEMORY AREA LOCATION
2677 011200 004362 177770  JSR     R3,-10(R2) /IN CASE WE DON'T ABORT
2678 011210 104003      MLY      *1      /GO TO PARITY MEMORY AREA
2679 011210 000410      BR      *+22     /DIDN'T ABORT
2680 011210 002777 000001 170400 Z1:  BIC     @BIT0,@PARITY /DISABLE PARITY
2681 011220 004037 011550  JSR     R0,@CHECKLOC /CHECK FOR GOOD ABORT
2682 011220 104003      MLY      *3      /ABORTED INCORRECTLY
2683 011230 012700 001100  MOV     @STACK,SP /RESET THE STACK
2684      /*****
2685 ITEST 65      TEST   BPL INSTRUCTION (=BOK)
    
```

```
2700 *****  
2701 YRTAG1 SCOPE  
2702 11745 *** ROM STATE 324 ***  
2703  
2704 11745 *** ROM STATE 1 ***  
2705 JSR RC,(R0) ;SET UP PARITY VECTOR SERVICE  
2706 ZR ;ROUTINE ADDRESS  
2707 BIC #BIT2,#PARITY ;WRITE NORMAL  
2708 MOV R1,R2 ;SET UP FOR A DAY  
2709 SOB #4,R2 ;CALCULATE THE START ADDRESS  
2710 ;FOR THIS TEST  
2711 MOV #12700,(R4)+ ;MOVE THE INSTRUCTION  
2712 ;#MOV #12700 TO PARITY MEMORY  
2713 ;AREA  
2714 MOV #1,(R2)+ ;MOVE A #1 INTO NEXT PARITY  
2715 ;MEMORY AREA LOCATION  
2716 MOV #1401,(R2)+ ;MOVE THE INSTRUCTION  
2717 #B00,44 INTO NEXT PARITY  
2718 ;MEMORY AREA LOCATION  
2719 BIC #BIT2,#PARITY ;WRITE OTHER PARITY  
2720 TTY #CPR04 ;AND BE ON AN INTR  
2721 BCD 28 ;SEARCH IF NO  
2722 ;SINCE THE PC WILL BE UPDATED  
2723 ;ON THE PARITY ABORT  
2724 MOV R2,#R0000AT ;STORE THIS PC THAT SHOULD  
2725 ;BE PUSHED ON THE STACK  
2726 ;IF A PARITY ABORT OCCURS  
2727 ;SINCE THE PC WILL NOT BE  
2728 ;UPDATED ON THE PARITY ABORT  
2729 MOV #240,(R2)+ ;MOVE A #240 INTO NEXT  
2730 ;PARITY MEMORY AREA LOCATION  
2731 ER 18 ;CONTINUE WITH TEST  
2732 MOV #240,(R2)+ ;MOVE A #240 INTO NEXT  
2733 ;PARITY MEMORY AREA LOCATION  
2734 MOV R2,#R0000AT ;STORE THE PC THAT SHOULD  
2735 ;BE PUSHED ON THE STACK  
2736 ;IF A PARITY ABORT OCCURS  
2737 BIC #BIT2,#PARITY ;WRITE NORMAL  
2738 MOV #R2,R2 ;MOVE #R2 #1 INTO NEXT  
2739 ;PARITY MEMORY AREA LOCATION  
2740 ;IN CASE WE DON'T ABORT  
2741 GO TO PARITY MEMORY AREA  
2742 ;IDENTIFY ABORT  
2743 GO TO NEXT TEST  
2744 BIC #BIT2,#PARITY ;CHECKABLE PARITY  
2745 JSR R0,#CHECKLOC ;CHECK FOR GOOD ABORT  
2746 HLT #3 ;ABORTED INCORRECTLY  
2747 MOV #STACK,SP ;RESET THE STACK  
2748 *****  
2749 /TEST 66 END OF PROGRAM  
2750 YRTAG1 SCOPE  
2751 BIC #BIT15:10:#BIT2,#PARITY ;DISABLE ALL PARITY  
2752 ;AND CLEAR ERROR BIT!  
2753
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2743 B11462 095737 001030 TST #USERTYPE ;DID THE USER SELECT THE REGISTRY  
2744 011400 001014 BNE 18 ;BRANCH IF SO AND DON'T STEP  
2745 ;UP THE TABLE  
2746 011410 092737 000002 001330 ADD #2,#R0000AD ;STEP UP TO NEXT REGISTER  
2747 011416 092737 000002 001414 ADD #2,#R0000AD ;STEP UP TO CORRESPONDING  
2748 ;PARITY MEMORY  
2749 011424 092737 000002 001416 ADD #2,#R0000AD ;STEP UP TO NEXT OFFSET IF THIS  
2750 ;IS ONLY APPLICABLE IF MEMORY  
2751 ;MIGHT BE TURNED ON  
2752 011432 092737 000002 001444 ADD #2,#R0000AD ;STEP UP TO NEXT INTERLEAVE  
2753 ;VALUE  
2754 011440 094337 011474 18 JSR R3,#FLAGCLR ;CLEAR PERTINENT FLAGS  
2755 011444 000167 000320 JMP SEOP ;GO TO RING-A-DING  
2756 ;BEFORE REITERATING THE PROGRAM  
2757 *****  
2758 ;  
2759 ;ROUTINE FOR SETTING UP THE PARITY VECTOR SERVICE ADDRESS  
2760 ;  
2761 *****  
2762 011450 012077 170144 VECSET1 MOV (R0)+,#INVEC ;WRITE ADDRESS INTO LOCATION 114  
2763 011454 092777 000005 170140 BIC #BIT2:BIT0,#PARITY ;WRITE OTHER PARITY AND DISABLE  
2764 011462 000200 RTS R0 ;RETURN TO TESTING  
2765 *****  
2766 ;  
2767 ;ROUTINE TO RESET AND GO BACK TO TABLE BEGINNING  
2768 ;  
2769 *****  
2770 011464 004337 011506 RESTART1 JSR R3,#INITIALIZE ;GO TO RING-A-DING  
2771 011470 000167 000274 JMP SEOP ;GO TO RING-A-DING  
2772 *****  
2773 ;  
2774 ;ROUTINE TO CLEAR PERTINENT FLAGS BEFORE PASSING INRU THE PROGRAM  
2775 ;WITH ANOTHER TABLE ENTRY  
2776 ;  
2777 *****  
2778 011474 005037 001674 FLAGCLR1 CLR #R0000ZONES ;CLEAR PC, PC AND ZONES ABORT  
2779 ;FLAG  
2780 011500 005037 001626 CLR #R0000RFLAG ;CLEAR MS11 REGISTER PRESENCE  
2781 ;FLAG  
2782 011504 000203 RTS R3 ;RETURN  
2783 *****  
2784 ;  
2785 ;ROUTINE TO COMPLETELY REINITIALIZE BEFORE RESTARTING PROGRAM OVER  
2786 ;AT THE BEGINNING OF THE TABLE  
2787 ;  
2788 *****  
2789 011506 005037 001624 INITIALIZE1 CLR #R0000ZONES ;CLEAR PC, PC AND ZONES  
2790 ;ABORT FLAG  
2791 011512 005037 001626 CLR #R0000RFLAG ;CLEAR MS11 REGISTER PRESENCE  
2792 ;FLAG  
2793 011516 012737 001340 001336 MOV #R0000,#R0000AD ;MOVE FIRST REGISTER CONTAINER
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INTO INPAD WHICH IS USED TO
 POINT TO THE PARITY REGISTER
 TABLE
 MOVE PARRY MEMORY CONTAINER
 INTO STMPD WHICH IS USED TO
 POINT TO THE MEMORY LOCATION
 TABLE
 MOVE FIRST OFFSET CONTAINER
 INTO OSEIAD WHICH IS USED TO
 POINT TO THE OFFSET LOCATION
 TABLE IF MEMORY HOLE IS USED
 MOVE 1ST INTERLEAVE CONTAINER
 INTO INTERAD WHICH IS USED
 TO POINT TO THE INTERLEAVE
 LOCATION TABLE
 RETURN

 SUBROUTINE TO CHECK THAT IF A TEST HAS OCCURRED IT DID IN THE
 AREA IN THE PROPER PLACE. IT IS QUITE CONCEIVABLE THAT A TEST
 THAT SHOULD HAVE OCCURRED IN THE 160 LOCATION MAP AREA ABOYD,
 FOR THE REASON OF THE INSTRUCTION THAT WAS TO CAUSE THE ABOYD,
 THIS SUBROUTINE WILL FLAG SUCH OCCURRENCES. WITHOUT THIS CHECK
 THE PROGRAM WOULD APPEAR TO HAVE RUN PROPERLY.

 BOTH THE CORRECT HIGH ORDER ERROR ADDRESS BITS AND PROPER PC
 PUSH ON THE STACK ARE LOOKED FOR AFTER A PARITY ABOYD OCCURS.

 CHECKLOC: NOV R2,*(SP) ;SAVE R2 CONTENTS ON STACK
 NOV R3,*(SP) ;SAVE R3 CONTENTS ON STACK
 NOV R4,*(SP) ;SAVE R4 CONTENTS ON STACK
 CLR R2 ;CLEAR ERROR ADDRESS BY COMPARE
 CLR R4 ;REGISTERS IN CASE HE HAVE AN OLD
 ;MOS DESIGN THAT DOESN'T HAVE
 ;ADDRESS BITS
 TST @#RREGFLAG ;IS AN M11 OR M111 PARITY
 ;OPTION BEING TESTED?
 BNE Y6 ;BRANCH IF M11 AND DON'T DO
 ;ADDRESS BITS CHECKING
 TST @#RSPCORZONES ;ARE WE DOING A 10 OR PC FETCH
 ;FOR ZONE ABOYD TEST?
 BNE Y5 ;BRANCH IF NO
 CLR R2 ;SET THE PARITY REGISTER HIGH
 ;ORDER ADDRESS BITS VALUE
 ;(BITS 5 THRU 11) TO ZERO FOR
 ;THE RS OR PC FETCH ABOYD TESTS
 ;GO CHECK IT AGAINST ACTUAL
 ;PARITY REGISTER VALUE
 ;MEMORY MANAGEMENT ONLY
 BNE Y4 ;BRANCH IF NO
 ;PICK UP THE OFFSET VALUE - IT

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SHOULD BE THE VALUE THAT WILL
 APPEAR IN THE PARITY REGISTER
 ERROR ADDRESS BITS
 GO TO CHECK1
 GET A FIRST POSSIBLE ABOYD
 LOCATION AREA
 GET THE FIRST POSSIBLE PARITY
 REGISTER HIGH ORDER ADDRESS
 BITS VALUE (BITS 5 THRU 11)
 IS THIS THE ABOYD AREA BEING
 TESTED?
 NO - SEE IF IT'S THE NEXT ONE
 PROCEED TO SEE IF
 IT WAS A PROPER ABOYD BY LOOKING
 AT THE HIGH ORDER ADDRESS BITS
 OF THE PARITY REGISTER
 (BITS 5 THRU 11)
 CLEAR ALL BITS EXCEPT 5 THRU 11
 ARE THE ERROR ADDRESS BITS
 WHAT THEY SHOULD BE?
 BRANCH IF NO
 STORE WHAT THE ADDRESS BITS
 SHOULD HAVE BEEN
 STORE WHAT THE ADDRESS BITS
 WERE
 RESTORE R4 CONTENTS
 RESTORE R3 CONTENTS
 RESTORE R2 CONTENTS

 IF WE HAVE REACHED THIS PATH 1 OF 2 CONDITIONS EXIST --
 WE HAVE AN OLD MOS DESIGN WITH NO ADDRESS BITS, OR WE HAVE
 MORE OR THE NEW MOS DESIGN WITH ADDRESS BITS OK!!
 WAS THE CORRECT PC PUSHED
 ON THE STACK?
 BRANCH IF YES
 WAS INCORRECT PC FOR PRINTOUT
 GO BACK TO INDICATE BAD ABOYD
 STEP UP RETURN ADDRESS
 TO BYPASS THE ERROR HLT
 RETURN TO CONTINUE TESTING
 STEP UP TO THE NEXT POSSIBLE
 ABOYD LOCATION AREA
 CHANGE THE VALUE OF THE HIGH
 ORDER ADDRESS BITS VALUE TO BE
 CHECKED
 GO BACK TO CHECK THIS ONE
 STORE WHAT THE ADDRESS BITS
 SHOULD HAVE BEEN
 STORE WHAT THE
 ADDRESS BITS WERE
 RESTORE R4 CONTENTS
 RESTORE R3 CONTENTS
 RESTORE R2 CONTENTS

```

2902
2903
2904
2905
2906 011746 026107 000000 00132L CMP 2(SB),##0000AT /WAS THE CORRECT PC PUSHED
2907 /ON THE STACK?
2908 011746 021804 BLD 100 /SEARCH IT YES
2909 011752 016607 000000 001330 MOV 2(SB),##0000AT /SAVE INDEPLY PC FOR FRINGHT
2910 011754 020000 /FO BACK TO INDICATE BAD ASOBY
2911 011750 021757 167340 001330 1031 MOV 0000AT,##0000AT /PREDIC TOE PASS THAT THE
2912 /CORRECT PC WAS PUSHED ON STACK
2913 011746 000200 RTS R0 /FO BACK TO INDICATE BAD ASOBY
2914
    
```

```

2915
2916
2917
2918
2919
2920
2921
2922 011770 000000
2923 011770 005067 167304
2924 011776 005067 000304
2925 012000 005067 167272
2926 012000 032157 000000 177570
2927 012014 001000
2928 012014 100000 012050
2929 012020 013720 000000
2930 012024 001411
2931 012030 002700 177777 000002
2932 012040 000000
2933 012042 000710
2934 012044 000200
2935 012046 000200
2936 012050 000200
2937 012052 000137 002020
2938 012056 177607 020377

/*****
/END OF PASS ROUTINE
/INCREMENT THE PASS NUMBER
/IF SWITCHING THE TTY BELL ON END OF PROGRAM
/IF THERE IS A MONITOR GO TO 15.
/IF NONE JUMP TO START
/PROG1 SCOPE
CLR STSNP /ZERO THE TEST NUMBER
CLR STNPS /ZERO THE NUMBER OF ITERATIONS
INC SPAND /INCREMENT THE PASS NUMBER
BIT #0010,##000R /RING THE BELL
BNE 48 /NO
TYPE #0011 /RING A BELL
MOV #0002,R0 /GET MONITOR ADDRESS
BEQ S00A0N /IF NONE
CMP #-1,R0
BNE SENDAD
RESET
SENDAD: JSR PC,(R0) /GO TO MONITOR
NOP /SAVE ROOM
NOP /FOR
NOP /FACT11
S00A0N: JHP #0001AT /RETURN
#0011: ,ASCIZ <207><377><377>
    
```

2930 2931 2932 2933 2934 2935 2936 2937 2938 2939 2940 2941 2942 2943 2944 2945 2946 2947 2948 2949 2950 2951 2952 2953 2954 2955 2956 2957 2958 2959 2960 2961 2962 2963 2964 2965 2966 2967 2968 2969 2970 2971 2972 2973 2974 2975 2976 2977 2978 2979 2980 2981 2982 2983 2984 2985 2986 2987 2988 2989 2990 2991 2992 2993 2994 2995 2996 2997 2998 2999

2993 2994 2995 2996 2997 2998 2999 3000 3001 3002 3003 3004 3005 3006 3007 3008 3009 3010 3011 3012 3013 3014 3015 3016 3017 3018 3019 3020 3021 3022 3023 3024 3025 3026 3027 3028 3029 3030 3031 3032 3033 3034 3035 3036 3037 3038 3039 3040 3041 3042 3043 3044 3045 3046

```

3051 012650 012665 000000 000000
3052 012650 012665 000000 000000
3053 012650 012665 000000 000000
3054 012650 012665 000000 000000
3055 012650 012665 000000 000000
3056 012650 012665 000000 000000
3057 012650 012665 000000 000000
3058 012650 012665 000000 000000
3059 012650 012665 000000 000000
3060 012650 012665 000000 000000
3061 012650 012665 000000 000000
3062 012650 012665 000000 000000
3063 012650 012665 000000 000000
3064 012650 012665 000000 000000
3065 012650 012665 000000 000000
3066 012650 012665 000000 000000
3067 012650 012665 000000 000000
3068 012650 012665 000000 000000
3069 012650 012665 000000 000000
3070 012650 012665 000000 000000
3071 012650 012665 000000 000000
3072 012650 012665 000000 000000
3073 012650 012665 000000 000000
3074 012650 012665 000000 000000
3075 012650 012665 000000 000000
3076 012650 012665 000000 000000
3077 012650 012665 000000 000000
3078 012650 012665 000000 000000
3079 012650 012665 000000 000000
3080 012650 012665 000000 000000
3081 012650 012665 000000 000000
3082 012650 012665 000000 000000
3083 012650 012665 000000 000000
3084 012650 012665 000000 000000
3085 012650 012665 000000 000000
3086 012650 012665 000000 000000
    
```

```

3087 012650 012665 000000 000000
3088 012650 012665 000000 000000
3089 012650 012665 000000 000000
3090 012650 012665 000000 000000
3091 012650 012665 000000 000000
3092 012650 012665 000000 000000
3093 012650 012665 000000 000000
3094 012650 012665 000000 000000
3095 012650 012665 000000 000000
3096 012650 012665 000000 000000
3097 012650 012665 000000 000000
3098 012650 012665 000000 000000
3099 012650 012665 000000 000000
3100 012650 012665 000000 000000
3101 012650 012665 000000 000000
3102 012650 012665 000000 000000
3103 012650 012665 000000 000000
3104 012650 012665 000000 000000
3105 012650 012665 000000 000000
3106 012650 012665 000000 000000
3107 012650 012665 000000 000000
3108 012650 012665 000000 000000
3109 012650 012665 000000 000000
3110 012650 012665 000000 000000
3111 012650 012665 000000 000000
3112 012650 012665 000000 000000
3113 012650 012665 000000 000000
3114 012650 012665 000000 000000
3115 012650 012665 000000 000000
3116 012650 012665 000000 000000
3117 012650 012665 000000 000000
3118 012650 012665 000000 000000
3119 012650 012665 000000 000000
3120 012650 012665 000000 000000
3121 012650 012665 000000 000000
3122 012650 012665 000000 000000
3123 012650 012665 000000 000000
3124 012650 012665 000000 000000
3125 012650 012665 000000 000000
3126 012650 012665 000000 000000
3127 012650 012665 000000 000000
3128 012650 012665 000000 000000
3129 012650 012665 000000 000000
3130 012650 012665 000000 000000
3131 012650 012665 000000 000000
3132 012650 012665 000000 000000
3133 012650 012665 000000 000000
3134 012650 012665 000000 000000
3135 012650 012665 000000 000000
3136 012650 012665 000000 000000
3137 012650 012665 000000 000000
3138 012650 012665 000000 000000
3139 012650 012665 000000 000000
3140 012650 012665 000000 000000
    
```

3141	013026	012067	000000	MOV	(R0)+,R0	PICK UP 'ERROR MESSAGE' POINTER
3142	013026	012067		TYPE		TYPE 'ERROR MESSAGE'
3143	013026	012067		E		'ERROR MESSAGE' POINTER GOES HERE
3144	013026	012067	012453	TYPE	,SCLRF	TYPE A CARRIAGE RETURN AND
3145						LINE FEED
3146	013026	012067	000000	MOV	(R0)+,R0	PICK UP 'DATA HEADER' POINTER
3147	013026	012067		R0	38	IF 'X' DUMP TYPE
3148	013026	012067		TYPE		TYPE 'DATA HEADER'
3149	013026	012067		B		'DATA HEADER' POINTER GOES HERE
3150	013026	012067	012453	TYPE	,SCLRF	TYPE A CARRIAGE RETURN AND
3151						LINE FEED
3152	013026	012067		MOV	(R0)+,R0	PICK UP 'DATA POINTER'
3153	013026	012067		BNE	58	IF THERE IS DATA TO TYPE GO ON TO
3154	013026	012067		MOV	(SP)+,R0	RESTORE NO
3155	013026	012067	012453	TYPE	,SCLRF	TYPE A CARRIAGE RETURN AND
3156						LINE FEED
3157	013026	012067		RTS	PC	RETURN TO TESTING
3158	013026	012067				
3159	013026	012067		MOV	0(R0)+,(SP)	SAVE 0(R0)+ FOR TYPEDOUT
3160						TYPE DATA
3161	013026	012067	000000	JBR	R0,582001	GO TYPE=OCTAL ASCII
3162	013026	012067	000	,BYTE	6	TYPE 6 DIGITS
3163	013026	012067	001	,BYTE	1	TYPE LEADING ZEROS
3164	013026	012067	003713	RT	(R0)	HAVE WE REACHED THE 'X' TERMINATOR
3165	013026	012067		BED	05	YES - CLEAR UP FOR RETURN
3166	013026	012067	013116	TYPE	,SP	TYPE 5 SPACES
3167	013026	012067		BR	55	FLOOR TILL 'X' TERMINATOR REACHED
3168	013026	012067	000000	,ANCTZ	/ /	
3169				,EVER		

3170						*****	
3171						*****	
3172						*****	
3173						*****	
3174						*****	
3175						*****	
3176						*****	
3177						*****	
3178						*****	
3179						*****	
3180	013124	012067		SIZE:	MOV	R0,(SP)	SAVE 00 CONTENTS
3181	013126	012067			MOV	SP,R0	SAVE THE STACK POINTER
3182	013126	012067	077406		MOV	R3,RKPAR3	
3183	013126	012067	148800		MOV	R3,RKPAR0	SET THE FOLLOWING PAGE
3184	013126	012067	165246		MOV	R3,RKPAR1	DESCRIPTOR REGISTERS TO
3185	013126	012067	155044		MOV	R3,RKPAR2	READ/WRITE AND TRANSFER OF
3186	013126	012067	155042		MOV	R3,RKPAR3	14896 (16) WORDS PER SEGMENT
3187	013126	012067	155040		MOV	R3,RKPAR4	
3188	013126	012067	155036		MOV	R3,RKPAR5	
3189	013126	012067	155034		MOV	R3,RKPAR6	
3190	013126	012067	155032		MOV	R3,RKPAR7	
3191	013126	012067	155030		CLR	RKPAR8	SET THE FOLLOWING PAGE
3192	013200	012777	000200	145024	MOV	R20,RKPAR1	ADDRESS REGISTERS TO THEIR
3193	013206	012777	000400	145020	MOV	R400,RKPAR2	RESPECTIVE OFFSET VALUES
3194	013214	012777	000600	145014	MOV	R600,RKPAR3	FOR RELOCATION PURPOSES
3195	013222	012777	001000	145010	MOV	R1000,RKPAR4	
3196	013230	012777	001200	145004	MOV	R1200,RKPAR5	
3197	013236	012777	001400	145000	MOV	R1400,RKPAR6	
3198	013244	012777	001600	144774	MOV	R1600,RKPAR7	
3199							THIS ONE'S THE I/O RECORD
3200							PAGE CONTAINING CONTROL STATUS
3201	013252	012704	144766		MOV	RPAR6,R4	REGISTERS, ETC.
3202	013256	012704			CLR	(R4)	GET ADDRESS OF PAGE 6 REGISTER
3203	013260	012704	144720		INC	R4	CLEAR THE REGISTER
3204	013264	012704			MOV	PC,(SP)	TURN ON MEMORY MANAGEMENT
3205	013266	012716	000024		ADD	05RTOUT,(SP)	MAKE KTI1 TIMEOUT SERVICE
3206							ROUTINE ADDRESS POSITION
3207	013272	012637	000004		MOV	(SP)+,05ENRVEC	INDEPENDENT
3208	013276	012637	143776		MOV	R4,R3776	SET FOR TIMEOUT
3209	013280	012714	000040		ADD	040,(R4)	TRAP ON NON-EXISTENT MEMORY
3210	013286	012714	144734		CMP	RKPAR7,(R4)	MAKE A 16 STEP
3211	013212	012637			BGT	15	LAST ONE?
3212	013314	011400			MOV	(R0),R0	NO - TRY IT!
3213	013316	152700	000040		MOV	R0,R0	GET LAST BANK +1
3214	013322	012704	000006	000004	MOV	R0,R0	DROP BACK
3215	013330	012637			MOV	R0,05ENRVEC	SET FOR ERRORS
3216	013332	012637	000010		MOV	R0,58P	RESTORE THE STACK POINTER
3217	013336	012637	144042		MOV	R0,58PBLK	STORE THE SAF
3218	013342	012637			CLR	R5	TURN MEMORY MGMT OFF
3219	013344	012637			MOV	(SP)+,R5	RESTORE R5
3220	013346	012637			RTS	PC	RETURN TO NORMAL FLOW
3221							CONTAINS THE SAF
3222							*****
3223							*****

```
3274 JCALL  
3275 J MOV NUM,*(SP) ;NUMBER TO BE TYPED  
3276 J JNR R0,80D0C ;CALL FOR TYPEOUT  
3277 J .BYTE N ;N#1 TO 6 FOR NUMBER OF DIGITS TO TYPE  
3278 J .BYTE M ;M#1 OR 0  
3279 J ; ;#TYPE LEADING ZEROS  
3280 J ; ;#SUPPRESS LEADING ZEROS  
3281 J  
3282 ;0001--ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST 0000 OR 00016  
3283 JCALL  
3284 J MOV NUM,*(CP)  
3285 J JNR R0,0001  
3286 J ;  
3287 ;00016--ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER  
3288 JCALL  
3289 J MOV NUM,*(SP)  
3290 J JNR R0,00016  
3291  
3292 013700 112067 00001 SPROCT: MOV (R0)*,00H00*1 ;PICKUP THE NUMBER OF DIGITS TO TYPE  
3293 013700 112067 00117S MOV (R0)*,00H11 ;GET THE ZERO FILL SWITCH  
3294 013700 000000 BR 0001 ;  
3295 013700 112167 00001 00010 000161 HORB 01,00FILL ;GET THE ZERO FILL SWITCH  
3296 013700 112767 00000 000107 HORB 16,00H00E01 ;GET THE SIX(8) DIGIT  
3297 013700 112767 00000 000146 00011 MOV 05,000H1 ;GET THE ITERATION COUNT  
3298 013700 013300 MOV R3,*(SP) ;SAVE R3  
3299 013700 012000 MOV R4,*(SP) ;SAVE R4  
3300 013700 012000 MOV R5,*(SP) ;SAVE R5  
3301 013712 116700 000127 MOVH S0H0E-1,R4 ;GET THE NUMBER OF DIGITS TO TYPE  
3302 013712 000000 NEG R4 ;  
3303 013700 000000 AND R0,R4 ;SUBTRACT IT FOR MAX. ALLOWED  
3304 013700 110007 000120 MOVH R0,S0H0E ;SAVE IT FOR USE  
3305 013700 110700 000117 MOVH 00FILL,R4 ;GET THE ZERO FILL SWITCH  
3306 013700 010000 000010 MOV 16(SP),R5 ;PICKUP THE INPUT NUMBER  
3307 013700 000000 CLR R0 ;CLEAR THE OUTPUT WORD  
3308 013700 000100 101 BR R5 ;ROTATE R00 INTO "0"  
3309 013700 000000 BR R5 ;GO DO R00  
3310 013700 000100 ROL R0 ;FORH THIS DIGIT  
3311 013700 000100 ROL R0 ;  
3312 013700 000100 ROL R0 ;  
3313 013700 000100 ROL R0 ;  
3314 013700 000100 MOV R0,R3 ;  
3315 013700 000100 ROL R0 ;GET L00 UP THIS DIGIT  
3316 013700 100007 000070 DECB 00H00 ;TYPE THIS DIGIT  
3317 013700 100010 100010 BPL 70 ;IF NO  
3318 013700 000700 BIC 0177770,R5 ;GET R00 UP JUNK  
3319 013700 001000 BNE 40 ;TEST FOR 0  
3320 013700 000700 TST R4 ;SUPPRESS THIS 0?  
3321 013700 001000 BEQ 50 ;IF YES  
3322 013700 000200 INC R0 ;DON'T SUPPRESS ANYMORE 0'S  
3323 013700 000700 BIS 00,R3 ;MAKE THIS DIGIT ASCII  
3324 013700 110367 000032 MOVH R3,R5 ;MAKE ASCII IF NOT ALREADY  
3325 013700 100000 TYPE ,05 ;SAVE FOR TYPING  
3326 013700 100367 000024 DECB 00CNT ;GO TYPE THIS DIGIT  
3327 013700 000367 000024 BGT 20 ;COUNT BY 1  
3328 013700 000367 ;IF MORE TO DO
```

```
3270 013530 000002 DLT 60 ;IF DONE  
3271 013530 000000 INC R4 ;INSURE LAST DIGIT ISN'T A BLANK  
3272 013530 000700 BR 20 ;GO DO THE LAST DIGIT  
3273 013530 012000 601 MOV (SP)+,R5 ;RESTORE R5  
3274 013530 012000 MOV (SP)+,R4 ;RESTORE R4  
3275 013530 012000 MOV (SP)+,R3 ;RESTORE R3  
3276 013530 012016 MOV (SP)+,(SP) ;GET THE STACK FOR RETURNING  
3277 013530 000200 RTS R0 ;RETURN  
3278 013530 000 ;STORAGE FOR ASCII DIGIT  
3279 013531 000 .BYTE 0 ;TERMINATOR FOR TYPE ROUTINE  
3280 013532 000 SUCNT: .BYTE 0 ;OCTAL DIGIT COUNTER  
3281 013533 000 SPFILL: .BYTE 0 ;ZERO FILL SWITCH  
3282 013534 000000 00H000 ;NUMBER OF DIGITS TO TYPE
```

```

3291
3292
3293
3294 013505 010106
3295 013506 010107 000002
3296 013507 010108
3297 013508 010109
3298 013509 010110 013574
3299 013574 010200
3300
3301
3302
3303
3304
3305 013574 011110
3306 013600 012036
3307 013602 012512
3308 013604 012314
    
```

```

J*****
JTRAP ROUTINE
SIRAP: MOV R0,*(SP) ;SAVE R0
MOV 2(SP),R0 ;GET TRAP ADDRESS
TST *(R0) ;BACKUP BY 2
MOV0 (R0),R0 ;GET RIGHT BYTE OF TRAP
MOV SIRPAD(R0),R0 ;INDEX TO TABLE
RTS R0 ;GO TO ROUTINE
    
```

```

JTRAP TABLE
J ROUTINE
J *****
SIRPAD: STYPE ;CALL*TYPE TRAP*(100400) ;TY TYPEOUT ROUTINE
SREAC ;CALL*GETCHR TRAP*(100402) ;TY TYPEIN CHARACTER ROUTINE
SREAS ;CALL*GETSTR TRAP*(100404) ;TY TYPEIN STRING ROUTINE
SACCEP ;CALL*ACCEPY TRAP*(100406) ;READ AN OCTAL NUMBER FROM TY
    
```

```

3309
3310
3311
3312 013626 012737 013636 000024
3313 013637 012737 009340 000026
3314 013638 010066
3315 013639 010106
3316 013640 010206
3317 013641 010306
3318 013642 010406
3319 013643 010506
3320 013644 010606 000074 000024
3321 013645 010706 013654 000024
3322 013654 010806
3323 013655 010906
3324
3325
3326 013656 011006 000000
3327 013657 011106 010054
3328 013658 011206 000050
3329 013659 011306
3330 013660 011406
3331 013661 011506
3332 013662 011606
3333 013663 011706
3334 013664 011806
3335 013665 011906
3336 013666 012006 013666 000024
3337 013667 012106 010150 000026
3338 013668 012206 013702
3339 013669 012306 012716 001706
3340 013670 000002
3341 013671 000000
3342 013672 000776
3343 013673 000000
3344 013674 005015 007520 002527
3345 013675 000122
3346
    
```

```

J*****
JPOWER DOWN ROUTINE
SPWRDN: MOV ;CALLUP,*SPWRVEC ;SET FOR LAST UP
MOV #00,*SPWRVEC ;PRIO:Y
MOV R6,*(SP) ;PUSH R6 ON STACK
MOV R1,*(SP) ;PUSH R1 ON STACK
MOV R2,*(SP) ;PUSH R2 ON STACK
MOV R3,*(SP) ;PUSH R3 ON STACK
MOV R4,*(SP) ;PUSH R4 ON STACK
MOV R5,*(SP) ;PUSH R5 ON STACK
MOV SP,*SAVR6 ;SAVE SP
MOV SPWRUP,*SPWRVEC ;SET UP VECTOR
HALT
DR ;*2 ;THANG UP
    
```

```

J*****
JPOWER UP ROUTINE
SPWRUP: MOV ;SAVR6,SP ;GET SP
CLR ;SAVR6 ;WAIT LOOP FOR THE TY
INC ;SAVR6 ;WAIT FOR THE INC
BNE IS ;OF WORD
MOV (SP)+,R5 ;POPP STACK INTO R5
MOV (SP)+,R4 ;POPP STACK INTO R4
MOV (SP)+,R3 ;POPP STACK INTO R3
MOV (SP)+,R2 ;POPP STACK INTO R2
MOV (SP)+,R1 ;POPP STACK INTO R1
MOV (SP)+,R0 ;POPP STACK INTO R0
MOV ;SPWRDN,*SPWRVEC ;SET UP THE POWER DOWN VECTOR
MOV #340,*SPWRVEC+2 ;PRIO:Y
;SPWR ;POWER FAIL MESSAGE
MOV ;BEGIN,(SP) ;RESTART AT BEGIN
RTI
SALLUP: HALT
BR ;*2
;SAVR6: 0
;SPWR: ,ASCIZ <15><12>"POWER"
;THE POWER UP SEQUENCE HAS STARTED
;BEFORE THE POWER DOWN HAS COMPLETE
;PUT THE SP HERE
;EVEN
    
```

*****					*****				
ERROR AND MESSAGE TABLE LOADMENTS									
*****					*****				
3377									
3378									
3379									
3380									
3381									
3382	013757	020104	052123	042100	EM11	.ASCIZ	/FIRST DIDN'T ABORT /		
3383	013758	020111	052106	042104					
3384	013761	020105	051117	042104					
3385	013774	020000							
3386	013776	020000	042520	042516	EM02	.ASCIZ	/FATAL ERROR TO PROGRAM /		
3387	014004	021100	047500	047500					
3388	014010	021100	042520	047500					
3389	014023	021100	042501	042500					
3390		000							
3391	014027	125	047502	047122	EM31	.ASCIZ	/ABORTED INCORRECTLY /		
3392	014034	022100	042500	041516					
3393	014042	021117	042522	042504					
3394	014050	020517	042500	000					
3395	014055	114	042117	042500	EM41	.ASCIZ	/NO PARITY MEMORY FOUND BELOW 26K /		
3396	014072	042000	042504	042000					
3397	014070	042500	042117	042104					
3398	014076	021100	042500	042500					
3399	014104	042500	042522	042500	EM51	.ASCIZ	/FIRST DIDN'T WORK /		
3400	014106	047500	041505	042516					
3401	014104	042120	042107	042502					
3402	014107	042000	000						
3403	014105	125	042503	042102	EM61	.ASCIZ	/USER SELECTED REGISTER NOT PRESENT /		
3404	014108	042523	042516	042104					
3405	014100	042100	042503	042516					
3406	014106	042511	042500	042102					
3407	014174	047516	042104	042100					
3408	014212	041500	047100	042104					
3409	014210	042000							
3410	014212	047516	042500	042104	EM71	.ASCIZ	/NO PARITY MEMORY FOUND AT ALL /		
3411	014220	042111	042131	042516					
3412	014226	047516	042502	042500					
3413	014230	042517	042116	042000					
3414	014248	042124	042101	042114					
3415	014250	042000							
3416	014252	042504	047104	042007	EM81	.ASCIZ	/DIDN'T ABORT OR RECOGNIZE STACK VIOLATION /		
3417	014260	042500	047502	042102					
3418	014266	047440	042102	042500					
3419	014274	047503	047107	042511					
3420	014302	042105	042125	041501					
3421	014310	042113	042506	042117					
3422	014316	042101	047511	042116					
3423	014324	042000							
3424	014326	042101	051117	042524	EM114	.ASCIZ	/ABORTED BUT STACK VIOLATION NOT RECOGNIZED /		
3425	014334	042100	042502	042104					
3426	014342	042123	041501	042113					
3427	014310	042520	042117	042101					

3428	014356	047511	042116	047516					
3429	014344	042124	042502	047503					
3430	014372	047107	042511	042105					
3431	014470	000							
3432	014433	125	042524	042503	EM121	.ASCIZ	/STACK VIOLATION PICKED UP BUT ABORT NOT RECOGNIZED /		
3433	014410	042500	047511	042514					
3434	014416	042524	047117	042004					
3435	014424	042511	042513	042104					
3436	014432	042125	041000	042125					
3437	014440	042500	047502	042122					
3438	014446	047000	042117	041000					
3439	014454	042507	042517	042516					
3440	014462	042502	042104	042000					
3441	014470	042100	042517	042522	DM11	.ASCIZ	/PROGRAM REGISTER/<15><12>		
3442	014476	042115	042000	042522					
3443	014504	042007	042123	042105					
3444	014512	042505							
3445	014514	042000	041520	042000					
3446	014522	042000	042000	042116					
3447	014530	042105	042000	041505					
3448	014538	042124							
3449	014540	042104	042505	042511	DM21	.ASCIZ	/ REGISTER/<15><12>		
3450	014546	042524	042522	042102					
3451	014553	125	042116	042105					
3452	014560	042000	041505	042124					
3453	014566	042120	042517	042502	DM31	.ASCIZ	/PROGRAM/<15><12>		
3454	014574	042515	042102						
3455	014577	040	042000	042103					
3456	014604	042120	042517	042522	DM41	.ASCIZ	/ PC/ UNDER TEST/		
3457	014612	042115	042000	042522					
3458	014620	042007	042123	042105					
3459	014626	042000	042000	042105					
3460	014634	042520	042103	042105					
3461	014642	042000	042000	042000					
3462	014650	042103	042525	042014					
3463	014656	042000	042000	042013					
3464	014664	041505	042524	042010					
3465	014672	042000	041501	042524					
3466	014700	042101	042505						
3467	014704	042000	041520	042000					
3468	014712	042000	042524	042116					
3469	014720	042105	042000	041505					
3470	014726	042124	042000	042104					
3471	014734	042122	041000	042111					
3472	014742	042123	042000	042104					
3473	014750	042122	041000	042111					
3474	014756	042123	042000	047502					
3475	014764	042122	042000	042103					
3476	014772	042000	042101	042117					
3477	015000	042124	041520	000					
3478	015005	120	047522	042107	DM51	.ASCIZ	/PROGRAM REGISTER EXPECTED/<15><12>		
3479	015012	042501	042000	041000					
3480	015020	042505	042511	042524					
3481	015026	042122	042000	042000					

POWER DOWN AND UP ROUTINES

```

3455 015030 001100 001100 002024
3456 015040 001504 001504 002024
3457 015050 000 001504 002024
3458 015060 000000 001504 002024
3459 015070 001116 001100 002024
3460 015080 001525 001100 002024
3461 015090 001101 001117 001124
3462 015100 001520 000
3463 015110 001100
3464 015120 001524 001002 000000 0151
3465 015130 001007 001005 0151
3466 015140 001320 000700 0151
3467 015150 001300 001002 001100 0151
3468 015160 001320 001322 001324 0
3469 015170 000000
3470 015180 001320 001322 001322 0151
3471 015190 000000
3472
3473 000001
    
```

SYMBOL TABLE

```

A 003752 ABORT 003430 ACCEPT= 100000 A0 000026
A1 004100 A2 004102 B 000030 BEGIN 001706
BIT0 000001 BIT00 000001 BIT01 000002 BIT02 000004
BIT03 000010 BIT04 000020 BIT05 000040 BIT06 000100
BIT07 000000 BIT08 000000 BIT09 001000 BIT10 000002
BIT11 000000 BIT11 000000 BIT12 001000 BIT13 001000
BIT14 000000 BIT15 000000 BIT16 000000 BIT17 001000
BIT18 000000 BIT19 000000 BIT20 001000 BIT21 001000
BIT2 000020 BIT20 001000 BLKCNT 001000 BAYVEC= 000014
B3 004312 B1 004370 B2 004000 B3 001536
B4 004612 B5 004706 B6 004706 C 000042
CHECKL 011550 COMPUT 003204 CPU40 001040 CC 000516
C1 005210 C2 005304 C3 005300 C4 005336
C5 005314 C6 005570 C7 005644 C8 005702
C 006020 D0 006000 D08 000000 DD1 005144
DHI 014478 DM2 014540 DH3 014566 DHI 014604
DHS 015005 DISPLA= 177570 DT1 015106 DT2 015114
DTS 015120 DT4 015124 DT5 015102 D0 006076
D1 006156 E 006320 EMTYEC= 000030 EM1 013752
EM10 014252 EM11 014326 EM12 014403 EM2 013776
EM3 014027 EM4 014055 EM5 014120 EM6 014145
EM7 014212 ERRVEC= 000004 E0 006326 E1 006502
E2 006510 F 006634 FLAGSC 011474
E3 006604 F1 006774 G 007056 GETCHR= 100402
GLYSTR= 104404 G2 007326 H 007140
HOLDLO 010302 H0 007150 HI 007134 H2 007242
K3 007216 H0 007222 INITIA 011506 INTERT 001644
INTVEC 001620 JOTVEC= 000000 KPAB0 000230 KPAN1 000232
KPAR2 000234 KPAR3 000236 KPAR4 000240 KPAR5 000242
KPAR6 000244 KPAR7 000246 KPAR8 000210 KPAR1 000212
KPAR9 000214 KPAR5 000222 KPAR2 000224
KPARC 001636 K 007374 MEMAD 001000 HRK0 000320
KAK1 006506 HSGTYP 002372 HSEGF 001000 N 000067
NEWSTK 001476 NEXT1 002514 NN 007450 NOMORE 003172
NOREG 005144 NYERAD 001448 NYER0 001450 NYER1 001452
NYER10 001470 NYER11 001472 NYER12 001474 NYER2 001454
NYER3 001456 NYER4 001460 NYER5 001462 NYER6 001464
NYER7 001466 ONETRY 003500 P 007526 PARCOR 003112
PARITY 001622 PARTST 003222 PC *X000007 PS 177776
PSPCOR 001624 PSW 177776 PNRVEC= 000024 R 007604
RED 010400 RESTAR 011464 RESTOR 001634 RESVEC= 000010
R0 *X000000 R1 *X000001 R2 *X000002 RESVEC* 000010
R4 *X000004 R5 *X000005 R6 *X000006 R3 *X000005
R7 *X000007 R00 000704 R01 177570
SAYLOC 010126 S00 000250 SP *X000000 SWR 000004
SR2 000326 STACK 001100 START 003620 SW2 000002
SW0 000001 SW00 000001 SW01 000002 SW02 000004
SW03 000010 SW04 000020 SW05 000040 SW06 000100
SW07 000200 SW08 000400 SW09 001000 SW1 000002
SW10 002000 SW11 004020 SW12 010000 SW13 020000
SW14 004000 SW15 100000 SW2 000004 SW3 000010
SW2 000020 SW5 000040 SW6 000100 SW7 000200
SW8 000000 SW9 001000 T 007750 TBITVE= 000014
YK6 012622 YK5 012620 TRAPVE= 000034 TRTYEC= 000014
    
```

